

# Building Extensible Program Logics through Effect Handlers

ZICHEN ZHANG, New York University, USA

SIMON ODDERSHEDE GREGERSEN, New York University, USA

JOSEPH TASSAROTTI, New York University, USA

One strategy for reasoning about programs that have certain kinds of effects is to use program logics that provide specialized rules for reasoning about these effects. However, *developing* program logics requires skills that are distinct from those needed for *using* program logics, making the development of new logics challenging and less accessible. Moreover, when developing new logics, it can be difficult to reuse components from prior logics or combine support for different effects.

In this paper, we propose an approach for building extensible program logics based on effect handlers. Our starting point is an expressive program logic for reasoning about programs written in a pure, sequential language with support for effect handlers. Within this language, we implement handlers that model concurrency, distributed execution, and crash-recovery behavior. Then, by proving properties about these handlers, we extend the program logic and derive expressive rules for reasoning about these effects. In some cases, this approach leads to stronger reasoning rules than those found in prior program logics targeting these features.

In addition, we develop a relational logic for proving contextual refinements between programs using effects. As with unary reasoning, handlers enable this relational logic to be developed in an extensible way.

## 1 Introduction

Program logics have proven to be a powerful tool for program verification. As a result, a variety of program logics have been developed for challenging program features, including pointers [43], concurrency [27, 38, 39], weak memory [16, 29, 31, 35, 52, 53], distributed execution [30, 44, 57], crash recovery [12–14, 37, 42], and randomness [2, 3, 5–8, 24, 48], among others.

Traditionally, a program logic is developed by proving that a collection of reasoning rules is sound with respect to an operational or denotational semantics for a language. However, following this traditional approach is challenging for several reasons. First, the development of program logics requires skills that are distinct from those needed for *using* program logics, making the development of new logics less accessible. Second, when developing new logics, it can be difficult to reuse components or port a particular feature from a different logic. In reality, many important computer systems *combine* many of the features described in the previous paragraph, yet developing logics that provide support for such combinations of features requires significant work.

Recently, Vistруп et al. [56] proposed an alternative approach to developing program logics that addresses the re-usability problem. Starting from a minimal, pure lambda calculus, they incrementally add effects to this language by giving a denotational semantics in terms of ITrees [59]. On the logic side, one gives rules that logically “interpret” or “handle” the events in the generated ITree. The soundness of the logic is established in a modular way by relating these logical handlers for events in the ITree to an interpretation function that “executes” the events. While this approach addresses the problem of re-usability, it does not address the first accessibility issue: it requires understanding the formalism of ITrees and their denotation, and the adequacy proofs require a form of reasoning that is different from the task of *using* the program logic to reason about programs. Moreover, it is unclear how to use ITrees to account for certain types of program effects or logic features, such as prophecy variables.

This paper advocates for an alternative approach to developing program logics by using *effect handlers* [40, 41] to model all program effects. Effect handlers are a language feature that enable

programmers to define custom effects in a modular and compositional way. Moreover, recent work has shown how to develop program logics for reasoning about effect handlers [17–20]. For example, using these logics, it is possible to verify an effect handler that implements a mutable state effect and derive a specification that resembles the usual separation logic rules for reasoning about pointers. As a result, reasoning about a client program that uses this state effect handler looks just like doing a standard separation logic proof about a program that uses built-in primitive state. However, prior program logics for effect handlers have considered a setting where effect handlers are added on top of a language that already has various other forms of primitive effects built in, such as mutable references and concurrency. This makes sense for verifying examples that involve a subtle interaction between primitive effects and effect handlers, but it means that the soundness proofs of these logics combine the complexity of primitive effects and effect handlers.

In this work, we instead use effect handlers to bootstrap an expressive program logic for a range of effects. Our starting point is a minimal core effect handler language called FicusLang that has just two primitive effects: non-deterministic choice and recording an action to a trace. For reasoning about programs written in FicusLang, we develop Ficus, a separation logic for effect handlers that is an adaptation of an earlier logic called Hazel [18]. Using FicusLang, we then implement handlers to model mutable state, shared-memory concurrency, distributed execution over unreliable networks, and crash-recovery with durable state. For each effect, we apply Ficus to verify the handler implementations and obtain proof rules that are analogous to the reasoning principles derived in prior specialized program logics for these effects.

The handler-based approach even allows us to derive *stronger* proof rules than prior work. There are two main reasons. First, with the handler-based approach, we can build up effects in a hierarchical way, using earlier effects in the definition of handlers for later effects. Then, when verifying those later effects, we can use the proof rules from the earlier effects. For example, we show in §5 how to derive local *prophecy variables* [1, 28] from a simpler global prophecy by implementing local prophecy variables as an effect handler. Later, when implementing the handlers for crashes and recovery in §6, we attach prophecy variables to non-deterministic choices made during crashes, which allows client proofs to reason about when crashes will occur.

A second source of stronger proof rules arises from using effect handlers to implement non-standard versions of effects that are easier to reason about. For example, our handler for concurrency generates fewer interleavings than a standard operational semantics for concurrency. As a result, the “invariant-opening” rule that we obtain for this handler is stronger than the standard rule from most concurrent separation logics (CSLs). To justify the use of these non-standard semantics, we must prove that they are equivalent in an appropriate sense to the standard semantics. To do so, we develop a new relational logic for effect handlers called Banyan.

**Contributions.** To summarize, our work makes the following contributions:

- We extend Hazel [18] to develop Ficus, an extensible unary program logic based on effect handlers (§2). Ficus uses *protocols* to decompose reasoning about handlers and client code using handlers, and adds support for an extensible notion of *worlds* to share resources between client code (§3).
- We develop a relational logic, Banyan, for proving contextual refinement in the presence of effect handlers (§4). Banyan adapts Ficus protocols to the relational setting, similarly allowing for reuse and extensibility.
- As case studies, we show how to reconstruct and extend features from existing program logics using our effect handler approach. This includes: (1) A derivation of *local* prophecy variables out of global prophecies, along with an approach to *implicitly* make prophecies without annotating a program with prophecy operations; (2) A logic for crash-recovery

$$\begin{aligned}
v &::= () \mid \text{rec } f x. e \mid \dots \mid \text{cont } N \\
e &::= v \mid x \mid e e \mid \dots \mid \text{do } e \mid \S(N)[v] \mid (\text{try } e \text{ with } v k \Rightarrow e \mid \text{ret } v \Rightarrow e) \mid \text{pick} \mid \text{observe } e \\
K &::= [] \mid e K \mid K v \mid \dots \mid \text{do } K \mid (\text{try } K \text{ with } v k \Rightarrow e \mid \text{ret } v \Rightarrow e) \\
N &::= [] \mid e N \mid N v \mid \dots \mid \text{do } N
\end{aligned}$$

Fig. 1. Syntax of values  $v$ , expressions  $e$ , evaluation contexts  $K$ , and neutral evaluation contexts  $N$ .

reasoning with asynchronous durable storage that recovers the features of Perennial [12], but with a simpler model, and novel support for *crash-aware* prophecy variables; (3) A logic for distributed systems with IronFleet-style [25] atomic blocks.

Our work is mechanized in the Iris separation logic framework [27] and the Rocq Prover. Our formalization is available in the supplementary material.

## 2 Program Logics by Effect Handlers

This section provides an overview of how to build up a program logic using effect handlers. After introducing FicusLang, we describe the core features of Ficus, and use them to develop proof rules for reasoning about shared-memory concurrency. An inference rule with premises  $P_1, \dots, P_n$  and conclusion  $Q$  should be read as a separation logic entailment of the form  $P_1 * \dots * P_n \vdash Q$ .

### 2.1 The FicusLang Calculus

FicusLang is a call-by-value ML-style lambda calculus with effect handlers. The syntax of FicusLang is shown in Figure 1. The expression **do**  $v$  raises an effect with value  $v$ , which will later be handled by the closest enclosing effect handler. Expression **try**  $e_0$  **with**  $v_1 k \Rightarrow e_1 \mid \text{ret } v_2 \Rightarrow e_2$  installs a *shallow* effect handler for  $e_0$ : it evaluates  $e_0$  until either  $e_0$  raises an effect or becomes a value. For the first case, the handler will have access to the value  $v_1$  raised by the effect and a continuation  $k$  that allows the handler to resume  $e_0$  from the point the effect was raised. For the second case, the handler will obtain the result of  $e_0$ , a value  $v_2$ . This type of handler is called a shallow handler because it will disappear in both cases, and the interpreter must reinstall the handler if the expression  $e_0$  may raise effects multiple times.<sup>1</sup> Continuations are used by applying them like functions.

In addition to effect handlers, FicusLang has two built-in primitive effects. The first is the **pick** expression, which non-deterministically evaluates to an arbitrary integer. The second is **observe**  $v$ , which performs a labeled transition with the label given by the value  $v$ . These **observe** statements are used as a form of ghost code and underlie our support for prophecy variables as discussed in §5.

Figure 2 shows an example of a handler implementing a global state effect supporting **read** and **write** operations. The handler uses a state-passing style. The recursive function **go** takes a continuation  $k$ , a value  $r$  to pass to the continuation, and the current global state  $\sigma$ . It runs the continuation under a handler that expects raised effects to be pairs of the form  $(\eta, v)$ , where  $\eta$  is a *tag* indicating whether the operation is a **read** or **write**. Based on the tag, it recursively calls **go** with the appropriate return value and updated state. If the tag does not match **read** or **write**, it re-raises the effect to allow composition with another handler for other effects.

### 2.2 Core Ficus Logic

To reason about programs written in FicusLang, we make use of Ficus, a separation logic built on top of the Iris framework [27], adapted from the Hazel logic for effect handlers [18]. This section first presents the basic core of Ficus, which is essentially a subset of Hazel. Later sections will describe additional generalizations that go beyond Hazel.

<sup>1</sup>Deep handlers, which are re-installed after an effect is raised, can be simulated with shallow handlers and recursion.

```

runstate  $\triangleq$   $\lambda$  main init. go main () init
where go  $\triangleq$  rec go  $k$   $r$   $\sigma$ .
      try  $k$   $r$  with
         $v$   $k$   $\Rightarrow$  match  $v$  with
          (read, ())  $\Rightarrow$  go  $k$   $\sigma$   $\sigma$ 
          | (write,  $y$ )  $\Rightarrow$  go  $k$  ()  $y$ 
          | ( $\eta$ ,  $v$ )  $\Rightarrow$  go  $k$  (do ( $\eta$ ,  $v$ ))  $\sigma$ 
        | ret  $v \Rightarrow v$ 

```

Fig. 2. Handler for a global state effect.

$\frac{\text{EWP-VALUE} \quad \Phi(v)}{\text{ewp } v \langle \Psi \rangle \{ \Phi \}}$	$\frac{\text{EWP-DO} \quad \Psi(v, \Phi)}{\text{ewp } \text{do } v \langle \Psi \rangle \{ \Phi \}}$	$\frac{\text{EWP-MONO} \quad \Psi \sqsubseteq \Psi' \quad \forall v. \Phi(v) \multimap \Phi'(v) \quad \text{ewp } e \langle \Psi \rangle \{ \Phi \}}{\text{ewp } e \langle \Psi' \rangle \{ \Phi' \}}$
$\frac{\text{EWP-FRAME} \quad R \quad \text{ewp } e \langle \Psi \rangle \{ \Phi \}}{\text{ewp } e \langle \Psi \rangle \{ v. R * \Phi(v) \}}$	$\frac{\text{EWP-PURE} \quad \text{ewp } e \langle \Psi \rangle \{ \Phi \} \quad e' \multimap^* e}{\text{ewp } e' \langle \Psi \rangle \{ \Phi \}}$	$\frac{\text{EWP-BIND} \quad \text{ewp } e \langle \Psi \rangle \{ v. \text{ewp } N[v] \langle \Psi \rangle \{ \Phi \} \}}{\text{ewp } N[e] \langle \Psi \rangle \{ \Phi \}}$

Fig. 3. Selected reasoning rules about the effect weakest precondition  $\text{ewp } e \langle \Psi \rangle \{ \Phi \}$ .

Ficus uses a weakest precondition assertion of the form  $\text{ewp } e \langle \Psi \rangle \{ \Phi \}$  for reasoning about programs. In this assertion,  $e$  is a program expression,  $\Phi$  is a postcondition, and  $\Psi$  is a *protocol* that describes the specifications for effect handlers that are active as  $e$  executes. This assertion says that if  $e$  executes in an environment with handlers satisfying  $\Psi$ , then evaluating  $e$  will not get stuck, and if  $e$  terminates with value  $v$ , then the assertion  $\Phi(v)$  will hold. More concretely, the protocol  $\Psi$  is a predicate of type  $\text{Val} \rightarrow (\text{Val} \rightarrow \text{iProp}) \rightarrow \text{iProp}$ , where the first argument is the value raised with an effect, and the second argument is the postcondition at the time the effect was raised. Throughout this paper, we require all protocols to be *monotonic* [17]. A protocol  $\Psi$  is monotonic if  $(\forall w. \Phi(w) \multimap \Phi'(w)) \vdash \Psi(v, \Phi) \multimap \Psi(v, \Phi')$  for all  $v, \Phi$ , and  $\Phi'$ . This essentially enforces a one-shot continuation discipline in the logic which simplifies our presentation and suffices for our purposes.

Figure 3 lists a selection of reasoning rules for the  $\text{ewp}$  assertion. Most of these rules are similar to standard weakest precondition rules in separation logics. The key rule for reasoning about effects is **EWP-DO**, which says that to raise value  $v$ , it suffices to show that the protocol holds for the value  $v$  and the current postcondition  $\Phi$ .

For example, for the state handler in Figure 2, we use the STATE protocol

$$\begin{aligned}
\text{READ}^\gamma(v, \Phi) &\triangleq \exists x. v = (\text{read}, ()) * S^\gamma(x) * (S^\gamma(x) \multimap \Phi(x)) \\
\text{WRITE}^\gamma(v, \Phi) &\triangleq \exists x, y. v = (\text{write}, y) * S^\gamma(x) * (S^\gamma(y) \multimap \Phi(())) \\
\text{STATE}^\gamma(v, \Phi) &\triangleq \text{READ}^\gamma(v, \Phi) \vee \text{WRITE}^\gamma(v, \Phi)
\end{aligned}$$

where  $S^\gamma(x)$  is a predicate that uses a piece of *ghost state* with the name  $\gamma$  to assert that the current value of the global state  $\sigma$  is  $x$ . The first component of the protocol is READ, which says that when the effect tag is **read**, then the client must show  $S^\gamma(x)$  for some  $x$ , in which case the protocol gives back  $S^\gamma(x)$  for proving the postcondition  $\Phi$  instantiated with the value  $x$ , indicating that the return value of the effect will be  $x$ . The second component is the WRITE protocol, which updates the given  $S^\gamma$  predicate from value  $x$  to the value  $y$  being written and returns back the unit value. Finally, STATE is the disjunction of these two protocols.

By applying **EWP-Do**, we obtain the following derived rules for reasoning with this protocol:

$$\begin{array}{c}
 \text{EWP-READ} \\
 \hline
 \text{ewp } \mathbf{do} \text{ (read, ()) } \langle \text{STATE}^Y \rangle \{v. v = x\}
 \end{array}
 \quad
 \begin{array}{c}
 \text{EWP-WRITE} \\
 \hline
 \text{ewp } \mathbf{do} \text{ (write, y) } \langle \text{STATE}^Y \rangle \{v. v = () * S^Y(y)\}
 \end{array}$$

**Installing Handlers.** So far, we have seen how a client can reason about effects when an appropriate protocol is part of the ewp assertion. Protocols are added to the ewp when a handler is installed using the **EWP-Try** rule shown below. Using the Ficus approach, we think of the language and logic as being extended to support new effects by adding handlers, so applying this rule forms the core proof obligation of a developer trying to extend the program logic.

$$\begin{array}{c}
 \text{EWP-TRY} \\
 \hline
 \text{ewp } e \langle \Psi \rangle \{ \Phi \} \quad (\forall v_2. \Phi(v_2) * \text{ewp } e_2 \langle \Psi' \rangle \{ \Phi' \}) \wedge \\
 (\forall v_1, k_1. \Psi(v_1, \lambda w. \text{ewp } k_1 w \langle \Psi \rangle \{ \Phi \}) * \text{ewp } e_1 \langle \Psi' \rangle \{ \Phi' \}) \\
 \hline
 \text{ewp } \mathbf{try } e \text{ with } v_1 k_1 \Rightarrow e_1 \mid \mathbf{ret } v_2 \Rightarrow e_2 \langle \Psi' \rangle \{ \Phi' \}
 \end{array}$$

Specifically, in the **EWP-Try** rule, we start with a protocol  $\Psi'$ , and end up with a protocol  $\Psi$  when reasoning about the expression  $e$  that runs with the new handler available. This rule has two premises. The first premise requires proving an ewp about  $e$  with the new protocol  $\Psi$ . As a result, this premise will be proved by a client who may now reason as if  $e$  has access to the new effects.

Meanwhile, the second premise makes up the proof obligation that justifies extending the logic with this new protocol. This premise is a logical conjunction with two parts. The first conjunct is for the case where  $e$  evaluates to a value without raising an effect and requires showing that  $e$ 's postcondition  $\Phi$  implies an ewp about the remaining expression  $e_2$ . The second conjunct is for the case where  $e$  raises an effect. Recall that when  $e$  raises an effect, from the client's perspective it must establish the protocol  $\Psi$ . Conversely, that means that here in this proof rule, we get the protocol  $\Psi$  instantiated with the value  $v_1$  raised with the effect, and a predicate that captures a specification for the continuation  $k_1$ . From this, we must prove an ewp about the handler code  $e_1$  that will run. These two conjuncts are joined with  $\wedge$  instead of  $*$  because only one of these two outcomes will occur, so the rule does not require separate resources for each conjunct.

To apply this rule for the state handler from [Figure 2](#), and thereby add the STATE protocol, we first need to more carefully define the  $S^Y(x)$  assertion. To do so, we use the underlying Iris logic's support for defining ghost state using *resource algebras* [27]. In particular, we define it as the *fragment* copy of a *authoritative* resource algebra:  $S^Y(x) \triangleq [\![\bullet x]\!]^Y$ . Roughly speaking, this resource algebra comes with two types of ghost resources: an authoritative copy  $[\![\bullet x]\!]^Y$  and a fragment copy  $[\![\circ x]\!]^Y$ . When these copies are combined together, they are guaranteed to agree, i.e.,  $[\![\bullet x]\!]^Y * [\![\circ y]\!]^Y \vdash x = y$ , and they can be updated to an arbitrary value  $y$ , with the rule  $[\![\bullet x]\!]^Y * [\![\circ x]\!]^Y \vdash \Rightarrow [\![\bullet y]\!]^Y * [\![\circ y]\!]^Y$ , where  $\Rightarrow$  is the *basic update modality*. The update modality is the primitive for manipulating ghost resources in the Iris logic. The assertion  $\Rightarrow P$  says that we can update our ghost resources and obtain  $P$ . The modality can be eliminated at any suitable time during program verification.

The handler `runstate` allocates ghost states  $[\![\bullet \mathit{init}]\!]^Y$  and  $[\![\circ \mathit{init}]\!]^Y$  at a fresh ghost location  $y$ , where  $\mathit{init}$  is the initial value for the state that is passed in. It keeps its authoritative copy  $[\![\bullet \mathit{init}]\!]^Y$ , and passes the fragment copy  $[\![\circ \mathit{init}]\!]^Y$ , which is  $S^Y(\mathit{init})$ , to the client. Whenever the client raises an effect, it must show the value satisfies  $\text{STATE}^Y$ , which is then passed to the handler code. The handler proof uses the  $S^Y(x)$  that is included in  $\text{STATE}^Y$  and combines it with its corresponding authoritative copy of the ghost state to carry out the read or write. Note that the handler recursively calls `go`, thereby re-installing the handler and running the continuation. To reason about this recursion, we use Löb induction from the underlying Iris logic [27]. Altogether, we obtain the

```

runconc  $\triangleq$   $\lambda main. go \{ \{ (main, (), \mathcal{M}) \} \}$ 
where go  $\triangleq$  rec go pool.
    let  $((k, r, t), pool) := \text{choose } pool \text{ in}$ 
    try  $k \ r \text{ with}$ 
         $v \ k \Rightarrow \text{match } v \text{ with}$ 
             $(\text{fork}, e) \Rightarrow go \{ \{ (e, (), C), (k, (), t) \} \} \uplus pool$ 
             $| \quad (\eta, v) \Rightarrow go \{ \{ (k, \text{do } (\eta, v), t) \} \} \uplus pool$ 
        | ret  $v \Rightarrow \text{if } t = \mathcal{M}^\times \text{ then } v$ 
            else  $go \{ \{ ((\lambda_. v), (), t^\times) \} \} \uplus pool$ 

```

Fig. 4. A handler for concurrency.

following derived rules for **go** and **run**, starting from a base protocol  $\perp$  defined by  $\perp(v, \Phi) \triangleq \text{False}$ .

$$\begin{array}{c}
 \text{EWP-STATEGO} \\
 \frac{\boxed{\bullet\sigma}^Y \quad \text{ewp } k \ r \langle \text{STATE}^Y \rangle \{ \Phi \}}{\text{ewp } go \ k \ r \ \sigma \langle \perp \rangle \{ \Phi \}}
 \end{array}
 \qquad
 \begin{array}{c}
 \text{EWP-STATERUN} \\
 \frac{\forall \gamma. S^Y(\text{init}) * \text{ewp } main \ () \langle \text{STATE}^Y \rangle \{ \Phi \}}{\text{ewp } run_{\text{state}} \ main \ \text{init} \langle \perp \rangle \{ \Phi \}}
 \end{array}$$

**Building a Hierarchy of Effects.** The previous example showed how to go from no effects (represented by protocol  $\perp$ ) to the state effect (protocol  $\text{STATE}^Y$ ). In practice, we want to accumulate effects by nesting additional handlers within the handler for  $\text{STATE}^Y$ . To that end, as in Hazel, we define a combinator  $\oplus$  on protocols by  $(\Psi_1 \oplus \Psi_2)(v, \Phi) \triangleq \Psi_1(v, \Phi) \vee \Psi_2(v, \Phi)$ . In other words,  $\Psi_1 \oplus \Psi_2$  represents that a client may choose to use effects from either of  $\Psi_1$  or  $\Psi_2$ , or both. For example,  $\text{STATE}^Y = \text{READ}^Y \oplus \text{WRITE}^Y$ . Applying this operation to protocols results in a “larger” protocol. This is formally captured by a preorder relation on protocols  $\Psi_1 \sqsubseteq \Psi_1 \oplus \Psi_2$ . Intuitively,  $\Psi_1 \oplus \Psi_2$  is larger than  $\Psi_1$  because the former permits the client to raise more kinds of effects. Using the **EWP-MONO** rule, we can generalize **EWP-READ** and **EWP-WRITE** accordingly: rather than requiring exactly the protocol  $\text{STATE}^Y$ , we require that the protocol is some  $\Psi$  such that  $\text{STATE}^Y \sqsubseteq \Psi$ .

Similarly, the **EWP-STATEGO** and **EWP-STATERUN** specifications for installing the handler do not need to start from the base protocol  $\perp$ . Instead, they can start from an arbitrary protocol  $\Psi$ , and—so long as  $\Psi$  does not already handle the tags for **read** and **write**—the client code would then operate with protocol  $\Psi \oplus \text{STATE}^Y$ . This enables the state handler to be composed with an arbitrary context of previously installed handlers, allowing a logic developer to mixin rules for state with other effects. A protocol is said to handle a set of tags  $T$  if  $\Psi(v, \Phi) \vdash \exists t, v. v = (t, v) \wedge t \in T$  for all  $v$  and  $\Phi$ . We write  $\text{tags}(\Psi)$  for the tags handled by  $\Psi$ . For the state protocol we would require  $\text{read}, \text{write} \notin \text{tags}(\Psi)$ . As another example of effects, we have implemented a handler for a heap effect with dynamically allocatable higher-order references and the ability to locally read and write from a given reference. This handler uses the  $\text{STATE}$  protocol to store a global map representing the heap, and provides its own  $\text{HEAP}$  protocol for clients to use.

In this way, we compositionally build logical support for a collection of effects starting from the  $\perp$  protocol, extending the core pure logic with support for these effects. This approach is grounded in an adequacy theorem, which shows that the logic starting with the  $\perp$  protocol is sound.

**THEOREM 2.1 (ADEQUACY, CORE FICUS).** *Let  $\varphi$  be a first-order predicate. If  $\vdash \text{ewp } e \langle \perp \rangle \{ \varphi \}$  is derivable, then executing  $e$  will not get stuck, and if  $e \rightarrow^* v$  then  $\varphi(v)$  holds.*

### 3 Concurrency and Extensible Worlds

In the effects we have seen so far, the handler always immediately returns control back to the client that raised the effect. However, for other kinds of effects, the handler may instead pass control to other client code. To reason about these kinds of handlers, we need to go beyond the core features of Ficus inherited from Hazel. This section describes a new feature in Ficus called *extensible worlds*.



A key example of an effect where this mechanism is needed is preemptive concurrency. Figure 4 shows an implementation of a concurrency handler. It depends on a bag (*a.k.a.* multiset) library for the thread pool *pool*. Each thread in *pool* is represented by a triple of (continuation, result of last effect, thread type), where the thread type can be either a main thread  $\mathcal{M}$ , a child thread  $\mathcal{C}$ , or their terminated variants  $\mathcal{M}^\times$  and  $\mathcal{C}^\times$ .

To execute one thread, the scheduler non-deterministically chooses one thread from *pool*, let it execute for as many pure steps as it can until it raises an effect or terminates. If the thread raises a *fork* effect, the scheduler will push both the new thread  $(e, (), \mathcal{C})$  and the old thread  $(k, (), t)$  to the thread pool. If the thread raises another effect, the scheduler will forward the effect to an outer handler by re-raising it, collect its result, and put the old thread back to the thread pool. Finally, if the thread terminates, the scheduler will not immediately terminate the whole system but mark the thread as terminated and put it back into *pool*. The scheduler will only exit when the main thread terminates the second time, allowing other threads to continue executing for some number of steps before the program exits.

Because the handler may pass control to other threads when an effect is raised, we now need to reason about coordination between threads, which is what extensible worlds will enable.

**Background: Iris Invariants and Fancy Updates.** To motivate extensible worlds, let us first recall how modern concurrent separation logics like Iris handle reasoning about interaction between different threads. By default, CSL allows for local reasoning about different threads in a concurrent system by dividing up state and resources into separate disjoint parts using separating conjunction, with each thread having ownership of some portion of state. However, in some cases, threads need to *share* ownership of state. To do so, CSLs make use of *invariants*. In Iris, an invariant assertion  $\boxed{P}^N$  says that  $P$  is an invariant that holds between all program steps. The  $N$  annotation is a *name* given to this invariant. These assertions are duplicable, meaning that  $\boxed{P}^N \vdash \boxed{P}^N * \boxed{P}^N$ , which allows each thread to have a copy of the assertion. When carrying out a proof about a thread, we access the underlying assertion  $P$  by “opening” the invariant using the following rule:

$$\frac{\text{WP-INVACC} \quad \boxed{P}^N \quad \mathcal{N} \subseteq \mathcal{E} \quad \triangleright P * \text{wp}_{\mathcal{E} \setminus \mathcal{N}} e \{x. \triangleright P * \Phi(x)\} \quad \text{atomic}(e)}{\text{wp}_{\mathcal{E}} e \{\Phi\}}$$

This rule allows us to prove the weakest precondition under the assumption that  $P$  holds (under a later modality  $\triangleright$  [4, 11, 36], which we will ignore for now), so long as we re-establish  $P$  in the postcondition of  $e$ . Here,  $e$  must be atomic, meaning that it reduces to a value in a single step, so that by re-establishing  $P$  in the postcondition, we ensure that  $P$  will continue to hold before and after each step. The *mask* parameter  $\mathcal{E}$  is a set that tracks invariants that have not yet been opened. The invariants in  $\mathcal{E}$  are said to be *closed* or *enabled*, while all other invariants are *open* or *disabled*.

In fact, in Iris, **WP-INVACC** is a derived rule. Iris uses a more primitive mechanism called a *fancy update modality* of the form  $\varepsilon_1 \Rrightarrow \varepsilon_2$  that encodes the process of opening and closing invariants. Informally, the assertion  $\varepsilon_1 \Rrightarrow \varepsilon_2 P$  is an assertion stating that starting with all invariants in  $\mathcal{E}_1$  being enabled, and then opening/closing invariants so as to end up with  $\mathcal{E}_2$  being enabled, it is possible to prove  $P$ . Then the **WP-INVACC** rule can be derived from the following two rules.

$$\frac{\text{FUPD-INVACC} \quad \boxed{P}^N \quad \mathcal{N} \subseteq \mathcal{E}}{\varepsilon \Rrightarrow \varepsilon \setminus \mathcal{N} \triangleright P * (\triangleright P * \varepsilon \setminus \mathcal{N} \Rrightarrow \varepsilon \text{ True})} \quad \frac{\text{WP-ATOMIC} \quad \varepsilon_1 \Rrightarrow \varepsilon_2 \text{wp}_{\varepsilon_2} e \{x. \varepsilon_2 \Rrightarrow \varepsilon_1 \Phi(x)\} \quad \text{atomic}(e)}{\text{wp}_{\varepsilon_1} e \{\Phi\}}$$

These rules are notationally heavy, but the rule on the left captures the process of opening an invariant with the update modality. Starting from masks in  $\mathcal{E}$ , we end up with masks in  $\mathcal{E} \setminus \mathcal{N}$ ,

and get  $\triangleright P$ . Additionally, we get that by supplying  $\triangleright P$  we can close the invariant, as represented by the  $\varepsilon_{\mathcal{N}} \models_{\varepsilon} \text{True}$ . Meanwhile, the rule on the right is what allows us to actually use the fancy update modality to open invariants when reasoning about an atomic expression  $e$ , so long as the the postcondition also includes a modality to close those same invariants.

Under the hood, the semantic model for this  $\varepsilon_1 \models_{\varepsilon_2}$  modality uses a mechanism called *world satisfaction*. Essentially, the Iris definition of  $\varepsilon_1 \models_{\varepsilon_2}$  tracks the set of all of the enabled/disabled invariants, and requires that for each enabled invariant, there are resources ensuring the invariant holds. This bundle of resources is called a world.

Although the Iris invariant mechanism is very expressive and flexible, it has some limitations. As a result, some prior projects have found it necessary to modify this notion of invariants. For example, both Perennial [12] and Nola [34] have considered alternate forms of invariant assertions, the former to encode invariants that govern behavior when a program crashes, and the latter to reason about termination without needing the later modality. One key issue, for our purposes, is that the notion of atomicity and the way invariants can be used in a rule like **WP-ATOMIC** is closely tied to the built-in preemptive concurrency in Iris. Instead, we want to allow handler implementers to define a notion of invariant suitable for the kind of effect they are modeling.

**Extensible Worlds.** To achieve this kind of extensibility, Ficus does not fix a single baked-in world in the interpretation of the fancy update. Instead, Ficus parameterizes the ewp and fancy update modalities by a customizable notion of world. The full version of the Ficus ewp assertion then has the form  $\text{ewp}_{W_1, W_2} e \langle \Psi \rangle \{ \Phi \}$ , where  $W_1$  is an arbitrary Iris proposition representing the world at the start of  $e$ 's execution, and  $W_2$  is the world after  $e$  finishes. Meanwhile, the fancy update modality becomes the *world update modality*  $w_1 \models_{w_2}$ , stating that the update is possible starting from the world  $W_1$  and ends up in the world  $W_2$ . When the starting world  $W$  is the same as the ending world, we simply write  $\text{ewp}_W e \langle \Psi \rangle \{ \Phi \}$  and  $\models_W$ .

Worlds are just normal Iris assertions, but it is nevertheless helpful to think of them more abstractly. The combination of two worlds, written  $W_1 \oplus W_2$ , is defined as  $W_1 * W_2$ . We impose a preorder  $\sqsubseteq$  on worlds defined by  $W_1 \sqsubseteq W_2 \triangleq \exists W'. (W_2 \dashv W_1 \oplus W')$ . Here, larger worlds have more resources, and the minimal element  $\perp$  is the proposition **True**. Thus, with an update like  $w_1 \models_{w_2} P$ , when  $W_2 \sqsubseteq W_1$ , we are shifting to a *smaller* world, and give the difference between  $W_2$  and  $W_1$  to the proof of  $P$ . Conversely, shifting to a larger world with  $W_1 \sqsubseteq W_2$  requires putting in the difference between  $W_1$  and  $W_2$ .

The rules we have seen previously for the ewp are generalized to account for worlds. A selection of the generalized rules about ewp and the world update modality are shown in **Figure 5**. **WUPD-INTRO** introduces a world update  $w_1 \models_{w_2} P$  by showing that, given access to the initial world  $W_1$ , we are able to prove  $P$  and the resulting world  $W_2$ , potentially performing ghost updates using the basic update modality. **WUPD-ELIM** eliminates a world update modality from an assumption, updating the worlds on the goal accordingly. The **WUPD-FRAME** allows for “framing out” an unnecessary world  $W$  that occurs in both the starting and ending world.

Unlike the Iris **WP-ATOMIC** rule, which only allows masks to change around an atomic step, the **EWP-WUPDPRE** rule allows us to apply a world update that changes the starting world for any expression, and **EWP-WUPDPOST** changes the corresponding ending world. This is allowed because, unlike standard Iris, where the scheduler could preempt a thread at any point, in the effect handler approach, control can only be transferred when an effect is raised. This means the starting world does not need to be immediately restored. Instead, only when an effect is raised, must the world be in an appropriate configuration, depending on whether the protocol  $\Psi$  requires it or not. In **EWP-DOWUPD**, we start by shifting to the bottom world  $\perp$ , and then in the continuation passed to



$$\begin{array}{c}
\text{WUPD-INTRO} \\
\frac{W_1 \multimap \models (P * W_2)}{W_1 \models_{W_2} P} \\
\\
\text{WUPD-ELIM} \\
\frac{Q \vdash_{W_2} \models_{W_3} P}{(W_1 \models_{W_2} Q) \vdash_{W_1} \models_{W_3} P} \\
\\
\text{WUPD-FRAME} \\
\frac{W_1 \models_{W_2} Q}{W_1 \oplus W \models_{W_2 \oplus W} Q} \\
\\
\text{EWP-VALUEWUPD} \\
\frac{W_1 \models_{W_2} \Phi(v)}{\text{ewp}_{W_1, W_2} v \langle \Psi \rangle \{ \Phi \}} \\
\\
\text{EWP-WUPDPRE} \\
\frac{W_1 \models_{W_2} \text{ewp}_{W_2, W_3} e \langle \Psi \rangle \{ \Phi \}}{\text{ewp}_{W_1, W_3} e \langle \Psi \rangle \{ \Phi \}} \\
\\
\text{EWP-WUPDPOST} \\
\frac{\text{ewp}_{W_1, W_2} e \langle \Psi \rangle \{ v. W_2 \models_{W_3} \Phi(v) \}}{\text{ewp}_{W_1, W_3} e \langle \Psi \rangle \{ \Phi \}} \\
\\
\text{EWP-DoWUPD} \\
\frac{W_1 \models_{\perp} \Psi(v, (\lambda r. \perp \models_{W_2} \Phi(r)))}{\text{ewp}_{W_1, W_2} \text{do } v \langle \Psi \rangle \{ \Phi \}} \\
\\
\text{EWP-WORLDFRAME} \\
\frac{\text{ewp}_{W_1, W_2} e \langle \Psi \rangle \{ \Phi \}}{\text{ewp}_{W_1 \oplus W, W_2 \oplus W} e \langle \Psi \rangle \{ \Phi \}}
\end{array}$$

Fig. 5. Selected reasoning rules for  $W_1 \models_{W_2}$  and  $\text{ewp}$  with worlds.

the protocol  $\Psi$ , we must restore back to  $W_2$ . Finally, we can frame out an unused world in  $\text{ewp}$  with **EWP-WORLDFRAME**.

**Recovering Iris Invariants.** It is straightforward to recover Iris-style impredicative invariants and the Iris fancy update modality in this more general world setting. As was described above, the standard Iris definition fixes some particular world in its definition of fancy updates, and uses ghost state to track the enabled invariants. Let us write  $\text{Tok}_I(\mathcal{E})$  for the assertion that bundles the world with the ghost state saying that mask  $\mathcal{E}$  is enabled. Then we recover the following analogue of the **FUPD-INVACC** rule that we saw earlier.

$$\begin{array}{c}
\text{WUPD-INVACC} \\
\frac{\boxed{P}^N \quad \mathcal{N} \subseteq \mathcal{E}}{\text{Tok}_I(\mathcal{E}) \models_{\text{Tok}_I(\mathcal{E} \setminus \mathcal{N})} \triangleright P * (\triangleright P \multimap_{\text{Tok}_I(\mathcal{E} \setminus \mathcal{N})} \models_{\text{Tok}_I(\mathcal{E})} \text{True})}
\end{array}$$

Moreover, by combining this rule with **EWP-WORLDFRAME**, we can support Iris invariants while including other possible components in the world. As we will see in §6.1, this allows us to encode a mechanism similar to Perennial’s crash borrows [49] while retaining standard Iris invariants.

**Protocol for the Concurrency Handler.** Now that we have an extensible mechanism for encoding invariants that hold across threads, we turn to the protocol for the concurrency handler.

One challenge is that the concurrency handler in Figure 4 is generic, in the sense that it does not know about the other effects that might be supported by outer handlers. It simply re-raises those effects to the outer handler and potentially transfers control to another thread when the effect returns. This means that the outer handlers could, say, implement shared memory or channel-based message passing concurrency, or some combination thereof. Ideally, the protocol we develop should similarly work for different kinds of outer effects.

To achieve this, the first ingredient is a *protocol transformer*  $\text{ATOM}^W$  that lifts a protocol for these outer effects into a concurrent protocol, where  $W$  is a world that describes shared resources that can be accessed by different threads. To do this lifting,  $\text{ATOM}$  transforms  $\Psi$  to ensure that as part of raising an effect governed by  $\Psi$ , the thread must be able to restore the world  $W$ . In addition, when the handler returns control back to a thread, it promises that  $W$  will hold. Formally, this is captured through the following definition

$$\text{ATOM}_W(\Psi)(v, \Phi) \triangleq \Psi(v, \lambda r. \perp \models_W W \models_{\perp} \Phi(r))$$

The first  $\perp \models_W$  is an obligation that the thread raising the effect has to be able establish  $W$  after the effect completes. Meanwhile, because the second  $W \models_{\perp}$  precedes the continuation  $\Phi(r)$ , it effectively gives back access to  $W$  before the continuation’s  $\Phi$  must be proved. In particular, if we

instantiate  $W$  to be  $\text{Tok}_I(\top)$ , where  $\top$  is the full mask saying that all invariants are enabled, then the above requires a thread to close all Iris invariants after the operation completes, just as in the Iris rule **WP-ATOMIC**.

To get the final protocol **CONC** for the concurrency handler, we combine **ATOM** with a protocol **FORK** governing the **fork** effect. Because the forked thread will run in the scope of the concurrency handler, **FORK** must have a recursive dependence on **CONC**.

$$\begin{aligned} \text{CONC}_W(\Psi) &\triangleq \text{ATOM}_W(\Psi \oplus \text{FORK}_W(\Psi)) \\ \text{FORK}_W(\Psi)(v, \Phi) &\triangleq \exists e. v = (\text{fork}, \lambda_. e) * \triangleright \text{ewp}_W e \langle \text{CONC}_W(\Psi) \rangle \{_. \text{True}\} * \Phi(()) \end{aligned}$$

Here, the recursive occurrence of **CONC** in **FORK** occurs under the later modality  $\triangleright$ , so that we can define the result as a *guarded fixed point* [15, 21, 27]. The protocol for **FORK** requires showing an appropriate *ewp* for the forked thread. Let us introduce a wrapper  $\text{fork } e \triangleq \text{do } (\text{fork}, \lambda_. e)$ . We can re-derive the standard **fork** rule from Iris with this protocol.

$$\frac{\text{EWP-FORK} \quad \text{ewp}_W e \langle \text{CONC}_W(\Psi) \rangle \{_. \text{True}\}}{\text{ewp}_W \text{fork } e \langle \text{CONC}_W(\Psi) \rangle \{v. v = ()\}}$$

Finally, we have the specification for **run<sub>conc</sub>** from Figure 4, which installs the concurrency handler.

$$\frac{\text{EWP-CONCRUN} \quad \text{fork} \notin \text{tags}(\Psi) \quad \text{ewp}_W \text{main } () \langle \text{CONC}_W(\Psi) \rangle \{\Phi\}}{\text{ewp}_W \text{run}_{\text{conc}} \text{main } \langle \Psi \rangle \{\Phi\}}$$

In addition to the ability to create threads with **fork**, we can also model primitive atomic instructions such as compare-and-swap (CAS) or fetch-and-add (FAA). To do so, we just need to define an additional handler on top of the heap handler and associated protocol **ATOMHEAP** that models these effects, much like the earlier **HEAP** protocol did. Combining **HEAP** and **ATOMHEAP** together, and applying the concurrency handler we obtain a protocol that models all of the operations one finds in the “standard” concurrent **HeapLang** distributed with Iris. Along the way, we have obtained a stronger version of the invariant opening rule, allowing us to keep invariants open for multiple pure steps.

However, a careful reader might object that what allowed us to derive this stronger invariant opening rule is the fact that the concurrency handler only transfers control to another thread when an effect is raised. In contrast, a standard operational semantics for preemptive concurrency typically allows for preemption at *every* step, thereby generating more possible interleavings of thread operations. Because our handler semantics for concurrency is not generating all of the interleavings that the standard semantics would, one might wonder whether this handler is really sound. Informally, the reason why the handler is sound in spite of this is that the intermediate pure steps in-between effects are not observable to other threads to the system, thus inserting additional preemption points would not change the possible outcomes of execution. In the next section, we introduce a *relational logic* that will allow us to prove this claim rigorously.

#### 4 Contextual Equivalence of Effectful Programs

In this section, we develop **Banyan**, a relational logic that allows us to prove correspondences between the behavior of two effectful programs written in **FicusLang**. Just as with unary reasoning, we compositionally derive relational reasoning rules for a number of effects. Using the resulting logic, we define logical relations models that can prove contextual equivalences of programs written in typed subsets of **FicusLang**. We apply this logical-relations model to prove that inserting additional preemption points in our concurrency handler does not change the set of possible

program behaviors, thus justifying the semantics from the previous section where preemption only occurs when effects are raised.

#### 4.1 Background: Embedding Relational Logics into Unary Logics

Banyan embeds relational reasoning into Ficus by encoding a second program as *ghost state*, as in CaReSL [51]. Let us first recall how this ghost state encoding works in modern Iris-based separation logics. For sequential programs, one first introduces two assertions:  $\text{spec}(e)$ , which says that the second program (which we call the “spec” program) is currently represented by the expression  $e$ ; and  $\text{specCtx}$ , which is an invariant that ensures that the  $\text{spec}(e)$  ghost state can only be updated in ways that represent valid transitions in the language’s operational semantics. We further add in assertions to represent the state of this spec program. For example a spec program points-to assertion  $l \mapsto_s v$  says that in the spec program, location  $l$  contains the value  $v$ , analogous to the standard points-to assertion. Using these assertions, one derives rules that allow for the spec program to be “executed” by applying ghost updates. To prove a relational property about two programs  $e_1$  and  $e_2$ , it then suffices to derive a judgement of the form:

$$\text{specCtx} * \text{spec}(e_1) \vdash \text{wp } e_2 \{v_2. \exists v_1. \text{spec}(v_1) * \varphi(v_1, v_2)\}$$

The soundness theorem of the encoding says that such a derivation implies that, for every execution of  $e_2$  terminating in a value  $v_2$ , there exists a terminating execution of  $e_1$  ending in some value  $v_1$  such that  $\varphi(v_1, v_2)$  holds. This basic approach can be generalized to account for concurrency as well by having multiple spec program resources, one for each thread in the concurrent program.

Banyan adapts this style of relational reasoning with ghost programs to the setting of effect handlers. A key challenge is that the usual ghost state encoding requires fixing the primitive effects of the language ahead of time, and requires special treatment in the concurrent case to introduce per-thread spec programs. In contrast, in Banyan these notions are derivable using protocols and worlds, just as with unary reasoning.

#### 4.2 Banyan: A Relational Logic for FicusLang

To enable extensibility, Banyan reasons about spec programs using an *effect specification resource* assertion  $\text{espec}_W e \langle \Psi \rangle$  that tracks a spec program  $e$  and a protocol  $\Psi$  in a world  $W$ . The program  $e$  can be updated and progressed according to the operational semantics of FicusLang. For example,  $\text{espec}_W ((\lambda x. e) v) \langle \Psi \rangle$  can be updated to  $\text{espec}_W e[v/x] \langle \Psi \rangle$  to reflect the execution of a beta reduction as justified by **ESPEC-PURE** shown below. As in Ficus, the protocol  $\Psi$  describes the effect handlers that are active as  $e$  executes. That is,  $\text{espec}_W N[\text{do } v] \langle \Psi \rangle$  can be updated to  $\text{espec}_W N[w] \langle \Psi \rangle$  such that  $\Phi(w)$  for some  $w$  by establishing  $\Psi(v, \Phi)$  as enabled by **ESPEC-DO**.

$$\begin{aligned} \text{espec}_W K[e] \langle \Psi \rangle * e \rightarrow^* e' &\vdash \models_W \text{espec}_W K[e'] \langle \Psi \rangle && \text{ESPEC-PURE} \\ \text{espec}_W N[\text{do } v] \langle \Psi \rangle * \Psi(v, \Phi) &\vdash \models_W \exists w. \text{espec}_W N[w] \langle \Psi \rangle * \Phi(w) && \text{ESPEC-DO} \end{aligned}$$

Using these rules, we obtain derived rules for raising specific effects like global state, much as in the unary case in §2, e.g.,

$$\begin{aligned} \text{espec}_W (\text{read}, ()) \langle \Psi \oplus \text{STATE}^Y \rangle * S^Y(v) &\vdash \models_W \text{espec}_W v \langle \Psi \oplus \text{STATE}^Y \rangle * S^Y(v) \\ \text{espec}_W (\text{write}, w) \langle \Psi \oplus \text{STATE}^Y \rangle * S^Y(v) &\vdash \models_W \text{espec}_W () \langle \Psi \oplus \text{STATE}^Y \rangle * S^Y(w) \end{aligned}$$

Similarly, we have rules for installing handlers that capture these effects, such as

$$\text{espec}_W \text{run}_{\text{state}} \text{main init} \langle \Psi \rangle \vdash \models_W \exists \gamma. S^Y(\text{init}) * \text{espec}_W \text{main} () \langle \Psi \oplus \text{STATE}^Y \rangle.$$

The following adequacy theorem for Banyan holds, which requires that both the spec protocol and the ewp protocol are  $\perp$ .

**THEOREM 4.1 (ADEQUACY, BANYAN).** *Let  $\varphi$  be a first-order relation. If*

$$\text{espec}_{\perp} e_2 \langle \perp \rangle \vdash \text{ewp}_{\perp} e_1 \langle \perp \rangle \{v_1. \exists v_2. \text{espec}_{\perp} v_2 \langle \perp \rangle * \varphi(v_1, v_2)\}$$

*and  $e_1 \rightarrow^* v_1$  then there exists a value  $v_2$  such that  $e_2 \rightarrow^* v_2$  and  $\varphi(v_1, v_2)$ .*

The key to proving this adequacy theorem lies in coming up with a suitable definition of the  $\text{espec}_W e \langle \Psi \rangle$  resource that validates the above rules.

**Constructing the Effect Specification Resource.** Much like the  $\text{ewp}$  assertion in Ficus, the specification resource  $\text{espec}_W e \langle \Psi \rangle$  tracks the behavior of the program  $e$  under the assumption that it executes in a program context satisfying the protocol  $\Psi$  and a logical world  $W$ . To define  $\text{espec}$ , we first define a more general construction  $\text{genspec}$  which we will specialize to obtain  $\text{espec}$ . The  $\text{genspec}$  assertion takes some abstract notion of a spec program and transforms it into a version that has protocols and worlds for reasoning about effects. Specifically, let  $\text{spec} : \text{Expr} \rightarrow \text{iProp}$  be a predicate with the property that  $\text{spec}(e) \vdash \models_W \text{spec}(e')$  when  $e \rightarrow^* e'$ . Given a choice of spec predicate, the  $\text{genspec}$  assertion is defined as

$$\text{genspec}_W e \langle \Psi \rangle \triangleq \exists K. \text{spec}(K[e]) * \text{handler}_W(\Psi)(K)$$

where the assertion  $\text{handler}_W(\Psi)(K)$  captures that  $K$  is an evaluation context that realizes the protocol  $\Psi$  indefinitely from the perspective of a program  $e$  running inside that context. Formally, this is expressed using a *greatest fixpoint*:

$$\text{handler}_W(\Psi) \triangleq \text{gfp } F, K.$$

$$\forall v. \text{spec}(K[v]) * \models_W \text{spec}(v)$$

$$\wedge \quad \forall v, N, \Phi. \text{spec}(K[\$(N)[v]]) * \Psi(v, \Phi) * \models_W \exists K', w. \text{spec}(K'[N[w]]) * \Phi(w) * F(K')$$

The two conjuncts of this definition require that

- (1) if  $e$  is a value  $v$  then the context terminates with value  $v$ , and
- (2) if  $e$  is a raised effect with continuation  $N$  and value  $v$ , i.e.,  $e = \$(N)[v]$ , and  $\Psi(v, \Phi)$  holds, then  $N$  is reinstated with some value  $w$  in a context  $K'$  such that  $\Phi(w)$  and  $\text{handler}_W(\Psi')(K')$  holds co-recursively.

We can derive generic versions of the **ESPEC-PURE** and **ESPEC-DO** rules for  $\text{genspec}$ , as well as examples like the STATE protocol. Then we obtain  $\text{espec}$  and specialized versions of these rules by instantiating  $\text{genspec}$  with  $\text{spec}(e) \triangleq e_0 \rightarrow^* e$ , where  $e_0$  is the initial expression that the ghost program starts as. Later, we will see how instantiating the definition with other choices of the base specification resource allow us to reason about thread-local effects in concurrent execution.

### 4.3 Concurrency

To reason relationally about effects that do not immediately transfer control back to the raising thread, we need to make use of extensible worlds, just as we did with the unary logic for concurrency. However, in the relational case, our specification of concurrency has an additional requirement: We want to be able to reason about each thread in the concurrent system individually. In the encoding of specification programs in CaReSL described above in §4.1, this is achieved by having a specification assertion per thread. Since each thread can raise effects and use other components of a protocol, we need these per-thread resources to have access to the protocol, just as in  $\text{espec}$ .

To construct this per-thread effect specification resource, we again use the  $\text{genspec}$  construction. To do so, we first need an underlying per-thread local specification resource  $\text{spec}_t^{\gamma}(e)$  that we will use to instantiate the construction with. Here, the  $\gamma$  parameter is a ghost name and  $t$  tracks whether

$$\begin{aligned} \text{spec}_t^Y(e) &\triangleq \exists k, r. [\circ\{(k, r, t)\}]^Y * (\forall K. \text{spec}(K[k \ r]) \multimap \models_{\perp} \text{spec}(K[e])) \\ \text{CTX}^Y(W, \Psi) &\triangleq \exists B, \text{pool}. \text{isBag}(B, \text{pool}) * [\bullet B]^Y * \text{espec}_W^{\text{go}_{\text{conc}}} \text{pool} \langle \Psi \rangle \end{aligned}$$

This thread-local specification resource fulfills the requirements needed to instantiate genspec, *i.e.*, when  $e \rightarrow^* e'$  then  $\text{spec}_t^Y(e) \vdash \mathbb{E}_W \text{spec}_t^Y(e')$ . When updating  $\text{spec}_t^Y(e)$  to  $\text{spec}_t^Y(e')$  in this derivation, instead of directly updating the underlying base effect specification resource in  $\text{CTX}^Y(W, \Psi)$ , we instead accumulate the evidence that there exists a thread in the pool that can be evaluated to the expression  $e'$  when it is next scheduled.

Let  $\text{espec}_w^{y:t} e \langle \Psi \rangle$  be notation for the result of instantiating genspec with this assertion. This thread-local effect specification resource gives us a unified mechanism to reason about both global and thread-local effects. It supports analogues of the **ESPEC-PURE** and **ESPEC-DO** rules. In addition, we derive a rule for forking threads,

$$\frac{\text{espec}_W^{Y:t} N[\text{fork } e] \langle \text{CONC}_s^Y(\Psi) \rangle \quad \text{CTX}^Y(W', \Psi') \sqsubseteq W}{\models_W \text{espec}_W^{Y:t} N[()] \langle \text{CONC}_s^Y(\Psi) \rangle * \text{espec}_{\text{CTX}^Y(W', \Psi')}^{Y:\mathcal{C}} e \langle \text{CONC}_s^Y(\Psi') \rangle}$$

where  $\text{CONC}_s^\gamma(\Psi) \triangleq \text{FORK}_s^\gamma \oplus \Psi$  and  $\text{FORK}_s^\gamma(v, \Phi) \triangleq \exists e. v = (\text{fork}, \lambda_{-}. e) * (\text{spec}_C^\gamma(e) \multimap \Phi())$ . Note that **SPEC-FORK** assumes that the specification context is in the current world. The specification context is allocated when the concurrency handler is installed using the following rule.

$$\frac{\text{SPEC-CONC-RUN} \quad \text{espec}_W \text{run}_{\text{conc}} \text{main} \langle \Psi \rangle \quad \text{fork} \notin \text{tags}(\Psi)}{\models_W \exists \gamma. \text{CTX}^\gamma(W, \Psi) * \text{espec}_{\text{CTX}^\gamma(W, \Psi)}^{\gamma, \mathcal{M}} \text{main} () \langle \text{CONC}_s^\gamma(\Psi) \rangle}$$

#### 4.4 Logical Relation for Contextual Equivalence

Using Banyan, we next define a binary *program-logic based logical relation* [22] for proving contextual equivalence of programs written in typed subsets of FicusLang. Intuitively, an expression  $e_1$  is contextually equivalent to another expression  $e_2$  at a type  $\tau$ , written  $e_1 \simeq_{\text{ctx}} e_2 : \tau$ , if no well-typed contexts  $C$  can distinguish them. In other words, the behavior of a client program remains unchanged if we replace any occurrence of the sub-program  $e_1$  with  $e_2$ . Contextual equivalence is defined as the symmetric interior of contextual refinement, denoted by  $e_1 \lesssim_{\text{ctx}} e_2 : \tau$ . Intuitively refinement means that, for any context  $C$  the observable behavior of  $C[e_1]$  is *included* in the observable behavior of  $C[e_2]$ , *relative to a closing handler context  $H$* . Formally, we define

$$e_1 \lesssim_{\text{ctx}}^H e_2 : \tau \quad \triangleq \quad \forall b \in \text{Bool}, C : \tau \rightarrow \text{bool}. H[C[e_1]] \rightarrow^* b \Rightarrow H[C[e_2]] \rightarrow^* b.$$

As a consequence, both the context and the programs may interact through effects.

As an example, we consider a standard System-F-style type system  $\Theta \mid \Gamma \vdash e : \tau$  with impredicative polymorphism, recursive types, and typing rules for CAS, FAA, and the fork operation (see, e.g., Timany et al. [50] for a complete definition).

The logical relation is entirely standard and follows previous Iris-based models ([50]), *except* that we define the expression interpretation using Banyan instantiated with the atomic heap instructions and concurrency. The expression interpretation is

$$\llbracket \tau \rrbracket (e_1, e_2) \triangleq \forall N, t. \text{espec}_{W_s}^{y:t} N[e_2] \langle \Psi_2 \rangle \multimap \text{ewp}_W e_1 \langle \Psi_1 \rangle \{v_1. \exists v_2. \text{espec}_{W_s}^{y:t} N[v_2] \langle \Psi_2 \rangle * \llbracket \tau \rrbracket (v_1, v_2)\}$$

where  $\Psi_2 \triangleq \text{CONC}_s^Y(\Psi')$ ,  $\Psi_1 \triangleq \text{CONC}_W(\Psi)$ ,  $W_s = \text{CTX}^Y(\perp, \Psi_2)$ ,  $W = W_s \oplus \text{Tok}_I(\top)$  for  $\Psi$  and  $\Psi'$  that describe the global stack of effects (state, heap, and atomic heap). The proof of the fundamental theorem of logical relations is immediate from the existing proofs since all our rules for reasoning about the atomic heap operations and concurrency are identical to the usual separation logic rules.

**THEOREM 4.2 (FUNDAMENTAL).** *If  $\Theta \mid \Gamma \vdash e : \tau$  then  $\Theta \mid \Gamma \models e \lesssim e : \tau$ .*

To prove soundness, we consider the closing handler context

$$H_{\text{CONC}} = \text{run}_{\text{state}} (\lambda \_ . \text{run}_{\text{heap}} (\lambda \_ . \text{run}_{\text{atomheap}} (\lambda \_ . \text{run}_{\text{conc}} [])))$$

and use the handler rules, e.g., **EWP-CONC-RUN** and **SPEC-CONC-RUN**, and **Theorem 4.1**.

**THEOREM 4.3 (SOUNDNESS).** *If  $\cdot \vdash e_1 \lesssim e_2 : \tau$  then  $e_2 \lesssim_{\text{ctx}}^{H_{\text{CONC}}} e_2 : \tau$ .*

**Contextual Equivalence of Preemption.** Using our logical relation, we prove that inserting additional preemption points in concurrent programs does not change program behaviors. This justifies the soundness of using a scheduler that only triggers preemption when effects are raised, since it shows that additional preemption would not affect observable behavior. Formally, we introduce an expression **yield** that triggers a preemption point by defining **yield**  $\triangleq$  **fork** ( $\cdot$ ), i.e., a program that simply forks a thread that terminates immediately. By forking a thread, **yield** transfers control to the scheduler, which may choose another thread to continue.

To justify that **yield** has no effect on the computation, we show that it is contextually equivalent to the unit value, i.e., **yield**  $\simeq_{\text{ctx}}^{H_{\text{CONC}}} (\cdot) : \text{unit}$ . The proof is an immediate consequence of the rules **EWP-FORK** and **SPEC-FORK** for the left-to-right and right-to-left refinements, respectively. As a corollary, for example, it then follows that  $e_1; \text{yield}; e_2 \simeq_{\text{ctx}}^{H_{\text{CONC}}} e_1; e_2 : \tau$  for any well-typed  $e_1$  and  $e_2$ . As we will see in §7, a similar technique can be used to justify stronger atomicity reasoning rules in the context of distributed execution.

## 5 Case Study: Prophecy Variables

When verifying certain concurrent programs in a forward-reasoning style, at some points in the proof it is necessary to know how later operations will be non-deterministically ordered. Prophecy variables [1, 28] are a logical mechanism that allows for “speculating” or “predicting” these future outcomes during a proof. In Iris, these prophecy variables are ghost code, and a prover must instrument a program to attach prophecy variables to operations whose values need to be predicted. New prophecy variables are allocated using a command **newproph**, and then attached to a program value using **resolve**. The proof rules for prophecy variables tell us *at the time of allocation* what the future resolved value will be. Iris comes with an additional proof showing that these prophecy variable operations can be erased from the program without affecting the outcome.

In this section, we show how to extend Ficus with prophecy variables. Our starting point is a single *global* prophecy variable. On top of this global prophecy variable, we implement effect handlers that allow for dynamically allocatable local prophecy variables, with an interface similar



to that of Iris. Finally, we observe that by instrumenting the *handlers* for heap operations with prophecy variables, we can automatically extend all heap operations to have prophecies, without requiring the *client* program to be directly instrumented with prophecies.

**Global Prophecy.** Recall that FicusLang has a ghost expression **observe**  $v$ , which records the value  $v$  on a global trace. The entire future value of this trace is predicted in a global prophecy assertion  $\text{primProph}(\vec{v})$ , which is used when performing an **observe**.

$$\frac{\text{EWP-OBS} \quad \text{primProph}(\vec{v})}{\text{ewp}_w \text{ **observe** } w \langle \Psi \rangle \{ \_ . \exists \vec{v}' . \vec{v} = w :: \vec{v}' * \text{primProph}(\vec{v}') \}}$$

By making an observation of  $w$ , we immediately learn that the first element of  $\vec{v}$  is indeed  $w$ , so  $\vec{v}$  must equal  $w :: \vec{v}'$  for some unknown  $\vec{v}'$ .<sup>2</sup> The adequacy theorem of Ficus is extended to provide this prophecy assertion.

**THEOREM 5.1 (ADEQUACY, FICUS).** *Let  $\varphi$  be a first-order predicate. If  $\text{primProph}(\vec{v}) \vdash \text{ewp } e \langle \_ \rangle \{ \varphi \}$  is derivable for all  $\vec{v}$ , then executing  $e$  will not get stuck, and if  $e \rightarrow^* v$  then  $\varphi(v)$  holds.*

However, because this prophecy variable is global, it is awkward to use when trying to do local reasoning about data structures that need prophecies.

**Encoding Local Prophecy Variables.** We recover Iris-style local prophecy variables by using handlers on top of the global **observe**. Formally, our local prophecy variables are specified by the following protocols

$$\begin{aligned} \text{NEWPROPH}(u, \Phi) &\triangleq u = (\text{newproph}, ()) * (\forall p, \vec{v}. \text{proph}(p, \vec{v}) \multimap \Phi(p)) \\ \text{RESOLVE\_PROPH}(u, \Phi) &\triangleq \exists v, p, w, \vec{v}. u = (\text{resolve\_proph}, (v, p, w)) * \text{proph}(p, \vec{v}) * \\ &\quad (\forall \vec{v}' . \vec{v} = (v, w) :: \vec{v}' * \text{proph}(p, \vec{v}') \multimap \Phi(v)) \end{aligned}$$

A new prophecy variable is created by raising the effect **newproph**, which returns back a fresh prophecy variable  $p$ . Assertion  $\text{proph}(p, \vec{v})$  is the local version of  $\text{primProph}(\vec{v})$ , which says that the trace of resolutions that will occur on prophecy variable  $p$  is  $\vec{v}$ . The effect **resolve\_proph** resolves  $p$  to a pair of values  $(v, w)$ . The first component  $v$  is the primary value that we want to observe, while the second value is used for “tagging” meta-data to certain kinds of prophecies.

Under the hood, these assertions work by slicing the observation trace of the global prophecy into traces of individual prophecy variables. This is done by making every call to *ghostcodekeywordobserve* have the format  $(p, (v, w))$  where  $p$  is the identifier of the prophecy variable that the corresponding observation is for. Then we can (tentatively) define  $\text{proph}(p, \vec{v}) \stackrel{?}{=} \exists \vec{v}_0. \text{primProph}(\vec{v}_0) * \vec{v} = \text{filter}(p, \vec{v}_0)$ , where *filter* is the least fixed-point of

$$\begin{aligned} \text{filter}(p, (p', (v, w)) :: \vec{v}) &\triangleq \text{if } p = p' \text{ then } (v, w) :: \text{filter}(p, \vec{v}) \text{ else } \text{filter}(p, \vec{v}) \\ \text{filter}(p, \_ ) &\triangleq \varepsilon \end{aligned}$$

The filter projects out the observations that are associated with the indicated prophecy variable, and it returns  $\varepsilon$  as a default value, if the observations in the trace do not match the expected format.

Recall that resources in CSL are exclusive to one thread, so to actually have mutually independent prophecy variables, we need to decompose ownership of the global prophecy  $\text{primProph}(\vec{v})$  into

<sup>2</sup>As usual with prophecy reasoning, if the predicted value at the head of the sequence  $\vec{v}$  was *not*  $w$ , then we derive a contradiction from this rule, and no longer have to reason about this moot execution with a misprediction.

ownership of individual prophecy variables using an authoritative ghost resource algebra.

$$\begin{aligned} \text{proph}(p, \vec{v}) &\triangleq [\![\circ\{p \mapsto \vec{v}\}]\!]^{\gamma_p} \\ I_p &\triangleq \exists \vec{v}_0, M_p. \text{primProph}(\vec{v}_0) * [\![\bullet M_p]\!]^{\gamma_p} * \bigstar_{p \mapsto \vec{v} \in M_p} \vec{v} = \text{filter}(p, \vec{v}_0) \end{aligned}$$

Here  $\gamma_p$  is an arbitrary but fixed global variable. Invariant  $I_p$  connects individual prophecy variables to the global prophecy and is maintained by the handler. The underlying resource algebra guarantees that  $\circ\{p \mapsto \vec{v}\}$  is always an element in the map  $M_p$ .

The handler `runproph` provides protocols `NEWPROPH` and `RESOLVE_PROPH`. The handler maintains  $I_p$ . To create new prophecy variables, it internally maintains a monotonically increasing counter for the next fresh prophecy ID so that it can always “slice out” an unused resolving sequence from the global prophecy for a new prophecy variable. To resolve prophecy variable  $p$  to  $(v, w)$ , it makes an observation of  $(p, (v, w))$  and updates the ghost resources accordingly. In practice, the handler `runproph` is installed first so that other handlers can use prophecy variables.

**Atomic Prophecies.** The resolve effect we have seen so far resolves a value that is returned by evaluating some expression. In other words, the prophecy is resolved *after* the expression finishes. However, in some scenarios, it is necessary to atomically execute the expression and prophecy resolution at the same time, particularly for atomic heap operations like CAS and FAA. Iris provides support for this so-called atomic prophecy resolution, and we can also implement this on top of the local prophecy variables through another effect handler with the following protocol:

$$\begin{aligned} \text{RESOLVE}(\Psi)(v, \Phi) &\triangleq \exists e, p, w, \vec{v}. v = (\text{resolve}, (e, p, w)) * \text{proph}(p, \vec{v}) * \\ &\quad \Psi(e, (\lambda r. \forall \vec{v}'. \vec{v} = (r, w) :: \vec{v}' \multimap \text{proph}(p, \vec{v}') \multimap \Phi(r))) \end{aligned}$$

The handler `runatomproph` provides this protocol. To handle `do (resolve, (e, p, w))`, it executes `do (resolve_proph, (do e, p, w))`. By installing this handler *after* the `runproph` and the handlers for heap operations, but *before* the handler `runconc` for concurrency, we ensure that `do e` and `do (resolve_proph, ...)` behave as if they executed together atomically, because the additional `raise` in `runatomproph` does not trigger an additional preemption.

**Implicit Prophecies.** As in Iris, the above interface for prophecies still requires a proof developer to annotate a program with calls to create new prophecies and to resolve them at relevant points. However, we can use effect handlers to make it so that *every* heap location has an associated prophecy variable that predicts the full trace of operations that will be performed on that heap location. This “prophetic heap” handler interposes on all of heap related effect tags, and adds an extra `resolve` operation before re-raising the effect. With this protocol, when a location is allocated, in addition to the standard points-to assertion  $l \mapsto v$ , we also get a `proph(l,  $\vec{v}$ )` assertion. When a heap operation on  $l$  occurs, we also pass this `proph(l,  $\vec{v}$ )` assertion, allowing us to deduce that the value read/written to the location matches the head value in the trace  $\vec{v}$ . This allows for proofs with prophecies *without* having to annotate a client program with explicit prophecy operations. [Appendix B](#) describes the protocols in further detail.

## 6 Case Study: Crash-Recovery Reasoning

Many software systems that store data on durable media such as disks must be *crash safe*, meaning that the system must be able to recover from a crash caused by externally generated events such as power failures. When a crash occurs, any data that the system has in volatile memory, such as RAM, will be wiped, but data in durable storage will be preserved. After the system restarts, it will typically re-run a recovery procedure that restores system invariants. A number of program logics and verification frameworks have been developed for reasoning about such systems [12, 14, 37, 42].

```

runcrash_trigger  $\triangleq$   $\lambda$ main.
  try main () with
    v k  $\Rightarrow$  let r := do v in
      if nondet_bool () then do (crash, ())
      else k r
  | ret v  $\Rightarrow$  v

runcrash  $\triangleq$  rec run main.
  try main () with
    v k  $\Rightarrow$  match v with
      (crash, ())  $\Rightarrow$  observe (); run main
      | ( $\eta$ , v)  $\Rightarrow$  do ( $\eta$ , v)
  | ret v  $\Rightarrow$  v

```

Fig. 6. A model of crash and recovery execution.

In this section, we show how to model crashes and recovery with effect handlers, and apply Ficus to derive protocols for reasoning about these systems in the style of Perennial [12], a separation logic for reasoning about the combination of concurrency and crash safety.

The process of crashing and recovering is modeled by the pair of handlers in Figure 6. The handler `runcrash_trigger` is installed at the inner-most level of a stack of effects for each thread, allowing it to interpose on every `do`.<sup>3</sup> It handles these effects by non-deterministically choosing to either trigger a crash by raising `crash`, or by simply re-raising the effect and returning the result to the continuation. The second handler, `runcrash` responds to this trigger by throwing away the captured continuation  $k$  and re-starting the system by running `main`. Note that this handler does not directly deal with wiping the volatile state of the system. Instead, this is handled implicitly: by installing handlers for volatile state (such as the `heap` handler) *after* this crash handler (i.e., as part of `main`), this volatile state will be effectively thrown away as a result of re-running `main` from scratch. In contrast, durable state can be preserved by installing these handlers *before* installing the crash handler at an outer level.

### 6.1 Managing the Crash Invariant

In order to establish that a system is crash safe, it is essential to show that when the system restarts, the `main` procedure finds itself in a state that satisfies its precondition. Perennial maintains a global *crash invariant*  $\mathcal{R}$  that must hold before and after each step of execution, and which describes the durable state that the system needs upon restart.

**Local Crash Conditions.** Reasoning about a global crash invariant would run counter to the principle of local reasoning in concurrent separation logic. To recover per-thread reasoning about the crash invariant, Perennial extends the weakest precondition of each thread with an assertion called a *crash condition*. The crash condition enforces the portion of the global crash invariant that a given thread owns. In Ficus, rather than changing the weakest precondition to add an additional component, we can instead capture this local crash condition through worlds and a protocol transformer called DURA. We write  $\text{Tok}_C(R_c)$  for a world stating a thread is responsible for ensuring that the local crash invariant  $R_c$  holds before and after each step it takes. The DURA protocol forces a thread to show that this  $R$  holds before and after each step of execution.

$$\text{DURA}_W(\Psi)(v, \Phi) \triangleq \exists R_c. \Psi(v, \lambda r. \perp \Vdash_{W \oplus \text{Tok}_C(R_c)} (R_c \wedge_{W \oplus \text{Tok}_C(R_c)} \Vdash_{\perp} \Phi(r)))$$

Notice that  $R_c$  and the postcondition is connected by a logical conjunction  $\wedge$  because the program can only either crash or continue so only one of  $R_c$  and the postcondition will be used.

The derived proof rules for the crash handlers then require a specification for the top level `main` procedure of the following form.

$$\mathcal{R} \vdash \text{ewp}_{W \oplus \text{Tok}_C(\spadesuit \mathcal{R}), \perp} \text{main } () \langle \text{DURA}_W(\Psi) \rangle \{r. \exists R_c. \perp \Vdash_{W \oplus \text{Tok}_C(R_c)} R_c \wedge \Phi(v)\}$$

Here,  $\mathcal{R}$  is the global crash invariant that also serves as the precondition for `main`, and  $\spadesuit$  is the *post-crash modality* [47, 55] that captures how crashing modifies volatile and durable resources.

<sup>3</sup>To install `runcrash_trigger` for every child thread, our Rocq mechanization actually integrates `runcrash_trigger` into `runconc`.

Intuitively, this says that the main thread starts with precondition  $\mathcal{R}$ , and the initial crash condition requires  $\mathcal{R}$  holds after a crash.

**Concurrency with Crashes.** To add support for concurrency, we install the concurrency handler below the outer crash handler. Because of the crash condition, we need to strengthen the CONC protocol to a protocol called CRASHCONC.

$$\begin{aligned} \text{CRASHCONC}_W(\Psi) &\triangleq \text{DURA}_W(\Psi \oplus \text{FORK}'_W(\Psi)) \\ \text{FORK}'_W(\Psi)(v, \Phi) &\triangleq \exists e. v = (\text{fork}, \lambda_. e) * \\ &\triangleright \text{ewp}_{W \oplus \text{Tok}_C(\text{True}), \perp} e \langle \text{CRASHCONC}_W(\Psi) \rangle \{ \exists R_e. \perp \Vdash_{W \oplus \text{Tok}_C(R_e)} R_e \} * \Phi(()) \end{aligned}$$

CRASHCONC follows the same structure of CONC and is also a guarded fixed point. However, a forked child thread starts with a trivial crash condition and can terminate with any crash condition.

When forking a child thread, one would naturally like to move some resources from the parent thread to the child thread. Similarly, synchronization primitives like locks are logically thought of as transferring ownership of resources in CSL. However, in order to transfer ownership of durable resources that might be part of the crash condition, we also need a mechanism to transfer the *obligation* to maintain that part of the crash condition. Crash borrows in Perennial [49] provide a mechanism to “borrow” part of the crash condition as an ownable resource and transfer this resource to the child thread. A crash borrow  $\boxed{P \mid R}$  has *content*  $P$  that describes the resources currently contained, and an associated *crash obligation*  $R$ , where  $\Box(P * R)$ .

The crash borrow can be understood as a box that packages up a resource  $P$  while preserving the obligation  $R$  in the event of a crash. They are used through the following two key rules.

$$\begin{array}{c} \text{WUPD-CBRWALLOC} \\ \triangleright P \quad \Box(P * R) \\ \hline \text{Tok}_C(R_C * R) \Vdash_{\text{Tok}_C(R_C)} \boxed{P \mid R} \end{array} \qquad \begin{array}{c} \text{WUPD-CBRWRETURN} \\ \boxed{P \mid R} \\ \hline \text{Tok}_C(R_C) \Vdash_{\text{Tok}_C(R_C * R)} \triangleright P \end{array}$$

Rule **WUPD-CBRWALLOC** creates a crash borrow  $\boxed{P \mid R}$ . It consumes a resource  $P$  that is stronger than  $R$  and removes  $R$  from the crash condition. Rule **WUPD-CBRWRETURN** opens the box  $\boxed{P \mid R}$  to extract resource  $P$ , in exchange, it adds  $R$  to the crash condition. In Perennial, this crash borrow mechanism is encoded on top of standard Iris invariants in a complex manner that requires extensive use of later credits [45] to avoid inconsistencies from impredicative circularities. In Ficus, the encoding is considerably simpler, because we are able to use a separate world for managing crash conditions and crash borrows. The complete model can be found in [Appendix C](#).

## 6.2 Asynchrony and Crash-Aware Prophecies

Many durable storage media are *asynchronous*, meaning that when a write is performed, the written value does not immediately become durable. Instead, the written value is first stored in some volatile buffer and only later made durable. If a crash occurs while the value is still in the volatile buffer, then the write is lost. Reasoning about asynchrony is challenging when trying to prove that a concurrent durable data structure satisfies *durable linearizability* [26], because it makes the durability of an operation *future dependent*.

To deal with this challenge, Perennial introduced a *prophetic disk points-to* assertion of the form  $l \mapsto^d [v_c]v$  which says that the disk address  $l$  currently stores the value  $v$ , and after a crash occurs, the stored address will be  $v_c$ . In other words, this assertion bundles a normal points-to with a form of prophecy about the post-crash state. However, in Perennial, this primitive could not re-use the existing support for prophecies in Iris, and instead has an ad-hoc soundness proof. The issue is that, with standard Iris prophecy variables, there is no way to make a prophecy about whether an event will happen before or after a crash occurs.

In contrast, in Ficus it is easy to handle this by incorporating prophecy resolution as part of the implementation of the crash handler. We use the `observe ()` statement in `runcrash` to effectively record that a crash has occurred in the trace of every prophecy variable. Formally, for every prophecy variable  $p$ ,  $\text{proph } (p, \vec{v}) \vdash \blacklozenge(\vec{v} = \epsilon)$ . The definition of filter in §5 ensures that this truncates the trace of events in the prophecy stream for all variables. Thus, when inspecting the prophecy stream, it is possible to determine whether a crash will occur before the prophecy is resolved. We call these resulting prophecy variables *crash-aware*. Using this mechanism, we implement an asynchronous disk with prophetic points-to assertions by resolving a crash-aware prophecy whenever an asynchronous disk operation is performed. More details can be found in Appendix C.

## 7 Case Study: Distributed Systems with IronFleet-Style Atomic Blocks

In this section, we consider a distributed system with multiple nodes connected by an unreliable network, in which nodes communicate through messages that may be dropped, delayed, duplicated, or re-ordered. On top of the network, a global scheduler decides the order of execution of nodes.

**Network.** The network provides two operations.  $\text{NETWORK} \triangleq \text{SEND} \oplus \text{RCV}$ , where

$$\text{SEND}(v, \Phi) \triangleq \exists s, t, m, M. v = (\text{send}, (s, t, m)) * t \rightsquigarrow M * (t \rightsquigarrow \{(s, t, m)\} \cup M \multimap \Phi(()))$$

$$\text{RCV}(v, \Phi) \triangleq \exists t, M. v = (\text{recv}, t) * t \rightsquigarrow M * \left( \forall x. t \rightsquigarrow M * \left( \begin{array}{l} x = \text{inl } () \vee (\exists s, m. x = \text{inr } \\ (s, t, m) \wedge (s, t, m) \in M) \multimap \Phi(x) \end{array} \right) \right)$$

Assertion  $t \rightsquigarrow M$  says that  $M$  is the set of messages that have ever been sent to address  $t$ . Since messages can be arbitrarily duplicated, this set is monotonically increasing w.r.t. the subset relation. The SEND protocol expects a package of (source address, destination address, message) as input, and adds this package to the message history of the destination address. The RCV protocol expects the destination address  $t$  as input and non-deterministically chooses to either not return a message or to return an arbitrary message that has been sent to  $t$ . The dropping of a message is implicitly modeled as just never having it be selected for receipt. Protocol NETWORK is provided by the handler `runnetwork` which implements the network as a soup of messages [30, 58].

**Scheduler with IronFleet-Style Atomic Blocks.** Next, we need a scheduler `rundist` that specifies how nodes run concurrently through a  $\text{DISTR}_W^t$  protocol.

$$\text{DISTR}_W^t \triangleq \text{ATOM}_W(\text{SEND} \oplus \text{START}_W^t) \oplus \text{RCV}$$

$$\text{START}_W^t(v, \Phi) \triangleq \exists e. v = (\text{start}, \lambda_. e) * (t = C \vee \triangleright \text{ewp}_W e \langle \text{DISTR}_W^C \rangle \{ \_ . \text{True} \}) * \Phi(())$$

The `rundist` handler resembles `runconc` and potentially transfers control to different nodes when an effect is raised by a node. The START protocol is used for initially creating nodes. In the protocol above, the RCV effect is *outside* the ATOM protocol transformer. The reason for this is that `rundist` scheduler does *not* transfer control to another node when processing an `recv` operation. In other words, a node can receive a series of messages without transferring control to another node. This modeling choice is inspired by IronFleet [25] which uses a movers-based parallel reduction proof [32] to treat a sequence of receives followed by a sequences of sends as an atomic step. Our global scheduler permits the prover to view a series of `recv` operations, followed by a series node-local processing operations, followed by one `send` operation as an atomic block.<sup>4</sup> As a result, because the RCV is not included in the ATOM component, we do *not* need to close shared invariants when performing a RCV operation.

Even though this scheduler does not include preemptions at RCV, the absence of these preemptions does not affect the overall set of possible behaviors of the program. To prove this, we

<sup>4</sup>We preempt after a single send because a node could diverge after sending. IronFleet avoids this by proving total correctness.

apply a similar technique as in §4, and use Banyan to prove that an explicit **yield** preemption is contextually equivalent to the unit value. Thus, adding in additional preemptions does not change the program’s behavior. To carry out this proof, we develop a node-local specification resource  $\text{specn}_i^Y(e)$ , similar to the thread-local version described in §4. It also uses the evidence accumulation technique described in §4.3, but additionally accumulates the evidence that a node can delay message receipt without changing behavior. Intuitively, if a message  $m$  is received at some time  $T$ , then if we delay that receipt to some later time  $T'$ , it is still possible to receive  $m$ . We capture this evidence using a *monotonic* resource algebra to track the set  $M$  of messages. More details can be found in Appendix D.

## 8 Related Work

**Program Logics for Effect Handlers.** The most closely related work is the Hazel logic for effect handlers [18]. As discussed in §2 and §3, Ficus extends Hazel with support for extensible worlds.

Hazel only handles unary reasoning, whereas Banyan supports relational reasoning through an encoding into Ficus. Recently, de Vilhena et al. [20] developed Blaze, a relational logic for effect handlers. Like Banyan, Blaze builds on a unary logic and represents a specification program via ghost state. However, unlike Banyan, in which the unary logic and the specification program have separate protocols, Blaze instead provides a judgement with a *relational* protocol. They use this to prove refinements in which the interpretation of effects is different between the two programs. In contrast, our examples keep effects the same on both sides and prove that client programs under these effects are equivalent.

Among other examples, de Vilhena et al. [20] use Blaze to prove that a handler implementation of concurrency refines a primitive concurrency effect. This refinement is in some sense the opposite of the direction that motivated our refinement proof in §4: it essentially shows that for every execution of the concurrency handler (which only preempts at effects), there is a corresponding execution using primitive concurrency (which preempts at every step). In contrast, we show that inserting additional preemption points when using the concurrency handler does not generate new behaviors. This is morally equivalent to showing that the concurrency handler already covers all possible behaviors that could be generated by a full interleaving semantics. It would be interesting to apply Blaze’s approach to the kinds of applications we have considered here to justify the soundness of alternate implementations of effects that allow for deriving stronger reasoning rules.

Our logical relations are for type systems with a fixed collection of effects and do not provide rules for typing general effect handlers. Tes [19] and Affect [54] use logics to construct unary logical-relations models for type systems for effect handlers. Biernacki et al. [9, 10] directly construct a binary logical-relations model for effects and handlers using biorthogonality and step indexing.

**Extensible Program Logics.** As described in the introduction, Vistrup et al. [56] develop an approach to extensible program logics using ITrees [59]. They use a mechanism called *logical effect handlers* to interpret ITree events for an effect, which has similarities to the way Hazel and Ficus’s protocols give a logical interpretation of what a raised effect will do. Their soundness proofs relate these logical effect handlers to interpretations of the effects. In contrast, the corresponding soundness of a protocol in Ficus is justified by the rule for **try** that installs an effect handler and makes the protocol accessible. Since the handlers are themselves just programs written in FicusLang, one uses Ficus itself to prove these handlers implement the protocol. Thus there is no distinction between verifying a *program* and proving the soundness of an *extension* to the logic. Another difference is that using the effect handler approach, we are able to develop a relational logic by representing a specification program as ghost state. Vistrup et al. [56] do not develop a relational



logic. On the other hand, they show how to model other features, such as total correctness and angelic non-determinism, which we do not consider.

Like Ficus’s extensible worlds, Matsushita and Tsukada [34] parameterize the Iris update modality and Hoare triples by a notion of a world. However, they require that the update shifts to the same world before and after, *i.e.*, only considering shifts of the form  $w \models_W$ . Hence, they cannot model the use of extensible worlds in §3, in which invariants are kept open across non-preempting steps.

Dijkstra Monads [46] offer a framework for deriving pre- and postcondition reasoning about dependently-typed programs with monadic effects, and, more recently, some aspects of algebraic effect handlers [33]. However, Dijkstra Monads have not been applied to effects like concurrency, crashes, or distributed execution, which might be challenging to encode as monads in a compositional way. Existing work on Dijkstra Monads does not support relational reasoning.

## References

- [1] Martín Abadi and Leslie Lamport. 1988. The Existence of Refinement Mappings. In *Proceedings of the Third Annual Symposium on Logic in Computer Science (LICS '88)*, Edinburgh, Scotland, UK, July 5–8, 1988. 165–175. doi:10.1109/LICS.1988.5115
- [2] Alejandro Aguirre, Gilles Barthe, Marco Gaboardi, Deepak Garg, Shin-ya Katsumata, and Tetsuya Sato. 2021. Higher-order probabilistic adversarial computations: categorical semantics and program logics. *Proc. ACM Program. Lang.* 5, ICFP (2021), 1–30. doi:10.1145/3473598
- [3] Alejandro Aguirre, Philipp G. Haselwarter, Markus de Medeiros, Kwing Hei Li, Simon Oddershede Gregersen, Joseph Tassarotti, and Lars Birkedal. 2024. Error Credits: Resourceful Reasoning about Error Bounds for Higher-Order Probabilistic Programs. *Proc. ACM Program. Lang.* 8, ICFP (2024), 284–316. doi:10.1145/3674635
- [4] Andrew W. Appel, Paul-André Melliès, Christopher D. Richards, and Jérôme Vouillon. 2007. A very modal model of a modern, major, general type system. In *Proceedings of the 34th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages, POPL 2007, Nice, France, January 17–19, 2007*. 109–122. doi:10.1145/1190216.1190235
- [5] Jialu Bao, Emanuele D’Osualdo, and Azadeh Farzan. 2025. Bluebell: An Alliance of Relational Lifting and Independence for Probabilistic Reasoning. *Proc. ACM Program. Lang.* 9, POPL (2025), 1719–1749. doi:10.1145/3704894
- [6] Gilles Barthe, Thomas Espitau, Benjamin Grégoire, Justin Hsu, Léo Stefanescu, and Pierre-Yves Strub. 2015. Relational Reasoning via Probabilistic Coupling. In *Logic for Programming, Artificial Intelligence, and Reasoning - 20th International Conference, LPAR-20 2015, Suva, Fiji, November 24–28, 2015, Proceedings*. 387–401. doi:10.1007/978-3-662-48899-7\_27
- [7] Gilles Barthe, Justin Hsu, and Kevin Liao. 2020. A probabilistic separation logic. *Proc. ACM Program. Lang.* 4, POPL (2020), 55:1–55:30. doi:10.1145/3371123
- [8] Kevin Batz, Benjamin Lucien Kaminski, Joost-Pieter Katoen, Christoph Matheja, and Thomas Noll. 2019. Quantitative separation logic: a logic for reasoning about probabilistic pointer programs. *Proc. ACM Program. Lang.* 3, POPL (2019), 34:1–34:29. doi:10.1145/3290347
- [9] Dariusz Biernacki, Maciej Piróg, Piotr Polesiuk, and Filip Sieczkowski. 2017. Handle with care: relational interpretation of algebraic effects and handlers. *Proc. ACM Program. Lang.* 2, POPL, Article 8 (Dec. 2017), 30 pages. doi:10.1145/3158096
- [10] Dariusz Biernacki, Maciej Piróg, Piotr Polesiuk, and Filip Sieczkowski. 2020. Binders by day, labels by night: effect instances via lexically scoped handlers. *Proc. ACM Program. Lang.* 4, POPL (2020), 48:1–48:29. doi:10.1145/3371116
- [11] Lars Birkedal, Rasmus Ejlers Møgelberg, Jan Schwinghammer, and Kristian Støvring. 2012. First steps in synthetic guarded domain theory: step-indexing in the topos of trees. *Log. Methods Comput. Sci.* 8, 4 (2012). doi:10.2168/LMCS-8(4:1)2012
- [12] Tej Chajed, Joseph Tassarotti, M. Frans Kaashoek, and Nickolai Zeldovich. 2019. Verifying concurrent, crash-safe systems with Perennial. In *Proceedings of the 27th ACM Symposium on Operating Systems Principles, SOSP 2019, Huntsville, ON, Canada, October 27–30, 2019*. 243–258. doi:10.1145/3341301.3359632
- [13] Tej Chajed, Joseph Tassarotti, Mark Theng, Ralf Jung, M. Frans Kaashoek, and Nickolai Zeldovich. 2021. GoJournal: a verified, concurrent, crash-safe journaling system. In *15th USENIX Symposium on Operating Systems Design and Implementation (OSDI 21)*. USENIX Association, 423–439. <https://www.usenix.org/conference/osdi21/presentation/chajed>
- [14] Haogang Chen, Daniel Ziegler, Tej Chajed, Adam Chlipala, M. Frans Kaashoek, and Nickolai Zeldovich. 2015. Using Crash Hoare logic for certifying the FSCQ file system. In *Proceedings of the 25th Symposium on Operating Systems Principles, SOSP 2015, Monterey, CA, USA, October 4–7, 2015*. 18–37. doi:10.1145/2815400.2815402
- [15] Krzysztof Ciesielski. 2007. On Stefan Banach and some of his results. *Banach Journal of Mathematical Analysis* 1, 1 (2007), 1–10. doi:10.15352/bjma/1240321550

- [16] Hoang-Hai Dang, Jaehwang Jung, Jaemin Choi, Duc-Thuan Nguyen, William Mansky, Jeehoon Kang, and Derek Dreyer. 2022. Compass: strong and compositional library specifications in relaxed memory separation logic. In *Proceedings of the 43rd ACM SIGPLAN International Conference on Programming Language Design and Implementation* (San Diego, CA, USA) (PLDI 2022). Association for Computing Machinery, New York, NY, USA, 792–808. doi:10.1145/3519939.3523451
- [17] Paulo de Vilhena. 2022. *Proof of Programs with Effect Handlers. (Preuve de Programmes avec Effect Handlers)*. Ph.D. Dissertation. Paris Cité University, France. <https://tel.archives-ouvertes.fr/tel-03891381>
- [18] Paulo Emílio de Vilhena and François Pottier. 2021. A separation logic for effect handlers. *Proc. ACM Program. Lang.* 5, POPL, Article 33 (Jan. 2021), 28 pages. doi:10.1145/3434314
- [19] Paulo Emílio de Vilhena and François Pottier. 2023. A Type System for Effect Handlers and Dynamic Labels. In *Programming Languages and Systems - 32nd European Symposium on Programming, ESOP 2023, Held as Part of the European Joint Conferences on Theory and Practice of Software, ETAPS 2023, Paris, France, April 22-27, 2023, Proceedings*. 225–252. doi:10.1007/978-3-031-30044-8\_9
- [20] Paulo Emílio de Vilhena, Simcha van Collem, Ines Wright, and Robbert Krebbers. 2025. A Relational Separation Logic for Effect Handlers. (November 2025). <https://devilhena-paulo.github.io/files/blaze.pdf>.
- [21] Pietro Di Gianantonio and Marino Miculan. 2003. A Unifying Approach to Recursive and Co-recursive Definitions. In *Types for Proofs and Programs*, Herman Geuvers and Freek Wiedijk (Eds.). Springer Berlin Heidelberg, Berlin, Heidelberg, 148–161.
- [22] Derek Dreyer, Amal Ahmed, and Lars Birkedal. 2011. Logical Step-Indexed Logical Relations. *Log. Methods Comput. Sci.* 7, 2 (2011). doi:10.2168/LMCS-7(2:16)2011
- [23] Dan Frumin, Amin Timany, and Lars Birkedal. 2024. Modular Denotational Semantics for Effects with Guarded Interaction Trees. *Proc. ACM Program. Lang.* 8, POPL (2024), 332–361. doi:10.1145/3632854
- [24] Simon Oddershede Gregersen, Alejandro Aguirre, Philipp G. Haselwarter, Joseph Tassarotti, and Lars Birkedal. 2024. Asynchronous Probabilistic Couplings in Higher-Order Separation Logic. *Proc. ACM Program. Lang.* 8, POPL (2024), 753–784. doi:10.1145/3632868
- [25] Chris Hawblitzel, Jon Howell, Manos Kapritsos, Jacob R. Lorch, Bryan Parno, Michael Lowell Roberts, Srinath T. V. Setty, and Brian Zill. 2015. IronFleet: proving practical distributed systems correct. In *Proceedings of the 25th Symposium on Operating Systems Principles, SOSP 2015, Monterey, CA, USA, October 4-7, 2015*. 1–17. doi:10.1145/2815400.2815428
- [26] Joseph Izraelevitz, Hammurabi Mendes, and Michael L. Scott. 2016. Linearizability of Persistent Memory Objects Under a Full-System-Crash Failure Model. In *Distributed Computing - 30th International Symposium, DISC 2016, Paris, France, September 27-29, 2016. Proceedings (Lecture Notes in Computer Science, Vol. 9888)*, Cyril Gavoille and David Ilcinkas (Eds.). Springer, 313–327. doi:10.1007/978-3-662-53426-7\_23
- [27] Ralf Jung, Robbert Krebbers, Jacques-Henri Jourdan, Ales Bizjak, Lars Birkedal, and Derek Dreyer. 2018. Iris from the ground up: A modular foundation for higher-order concurrent separation logic. *J. Funct. Program.* 28 (2018), e20. doi:10.1017/S0956796818000151
- [28] Ralf Jung, Rodolphe Lepigre, Gaurav Parthasarathy, Marianna Rapoport, Amin Timany, Derek Dreyer, and Bart Jacobs. 2020. The future is ours: prophecy variables in separation logic. *Proc. ACM Program. Lang.* 4, POPL (2020), 45:1–45:32. doi:10.1145/3371113
- [29] Jan-Oliver Kaiser, Hoang-Hai Dang, Derek Dreyer, Ori Lahav, and Viktor Vafeiadis. 2017. Strong Logic for Weak Memory: Reasoning About Release-Acquire Consistency in Iris. In *31st European Conference on Object-Oriented Programming, ECOOP 2017, June 19-23, 2017, Barcelona, Spain*. 17:1–17:29. doi:10.4230/LIPICS.ECOOP.2017.17
- [30] Morten Krogh-Jespersen, Amin Timany, Marit Edna Ohlenbusch, Simon Oddershede Gregersen, and Lars Birkedal. 2020. Aneris: A Mechanised Logic for Modular Reasoning about Distributed Systems. In *Programming Languages and Systems - 29th European Symposium on Programming, ESOP 2020, Held as Part of the European Joint Conferences on Theory and Practice of Software, ETAPS 2020, Dublin, Ireland, April 25-30, 2020, Proceedings*. 336–365. doi:10.1007/978-3-030-44914-8\_13
- [31] Ori Lahav and Viktor Vafeiadis. 2015. Owicki-Gries Reasoning for Weak Memory Models. In *Automata, Languages, and Programming - 42nd International Colloquium, ICALP 2015, Kyoto, Japan, July 6-10, 2015, Proceedings, Part II*. 311–323. doi:10.1007/978-3-662-47666-6\_25
- [32] Richard J. Lipton. 1975. Reduction: A New Method of Proving Properties of Systems of Processes. In *Conference Record of the Second ACM Symposium on Principles of Programming Languages, Palo Alto, California, USA, January 1975*, Robert M. Graham, Michael A. Harrison, and John C. Reynolds (Eds.). ACM Press, 78–86. doi:10.1145/512976.512985
- [33] Kenji Maillard, Danel Ahman, Robert Atkey, Guido Martínez, Catalin Hritcu, Exequiel Rivas, and Éric Tanter. 2019. Dijkstra monads for all. *Proc. ACM Program. Lang.* 3, ICFP (2019), 104:1–104:29. doi:10.1145/3341708
- [34] Yusuke Matsushita and Takeshi Tsukada. 2025. Nola: Later-Free Ghost State for Verifying Termination in Iris. *Proc. ACM Program. Lang.* 9, PLDI (2025), 98–124. doi:10.1145/3729250
- [35] Glen Mével and Jacques-Henri Jourdan. 2021. Formal verification of a concurrent bounded queue in a weak memory model. *Proc. ACM Program. Lang.* 5, ICFP, Article 66 (Aug. 2021), 29 pages. doi:10.1145/3473571

- [36] Hiroshi Nakano. 2000. A Modality for Recursion. In *15th Annual IEEE Symposium on Logic in Computer Science, Santa Barbara, California, USA, June 26-29, 2000*. 255–266. doi:10.1109/LICS.2000.855774
- [37] Gian Ntzik, Pedro da Rocha Pinto, and Philippa Gardner. 2015. Fault-Tolerant Resource Reasoning. In *Programming Languages and Systems - 13th Asian Symposium, APLAS 2015, Pohang, South Korea, November 30 - December 2, 2015, Proceedings*. 169–188. doi:10.1007/978-3-319-26529-2\_10
- [38] Peter W. O’Hearn. 2007. Resources, concurrency, and local reasoning. *Theor. Comput. Sci.* 375, 1-3 (2007), 271–307. doi:10.1016/j.tcs.2006.12.035
- [39] Susan S. Owicki and David Gries. 1976. An Axiomatic Proof Technique for Parallel Programs I. *Acta Informatica* 6 (1976), 319–340. doi:10.1007/BF00268134
- [40] Gordon D. Plotkin and John Power. 2001. Adequacy for Algebraic Effects. In *Foundations of Software Science and Computation Structures, 4th International Conference, FOSSACS 2001 Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2001 Genova, Italy, April 2-6, 2001, Proceedings*. 1–24. doi:10.1007/3-540-45315-6\_1
- [41] Gordon D. Plotkin and Matija Pretnar. 2009. Handlers of Algebraic Effects. In *Programming Languages and Systems, 18th European Symposium on Programming, ESOP 2009, Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2009, York, UK, March 22-29, 2009, Proceedings*. 80–94. doi:10.1007/978-3-642-00590-9\_7
- [42] Azalea Raad, Ori Lahav, and Viktor Vafeiadis. 2020. Persistent Owicki-Gries reasoning: a program logic for reasoning about persistent programs on Intel-x86. *Proc. ACM Program. Lang.* 4, OOPSLA (2020), 151:1–151:28. doi:10.1145/3428219
- [43] John C. Reynolds. 2002. Separation Logic: A Logic for Shared Mutable Data Structures. In *17th IEEE Symposium on Logic in Computer Science (LICS 2002), 22-25 July 2002, Copenhagen, Denmark, Proceedings*. 55–74. doi:10.1109/LICS.2002.1029817
- [44] Upamanyu Sharma, Ralf Jung, Joseph Tassarotti, M. Frans Kaashoek, and Nickolai Zeldovich. 2023. Grove: a Separation-Logic Library for Verifying Distributed Systems. In *Proceedings of the 29th Symposium on Operating Systems Principles, SOSOP 2023, Koblenz, Germany, October 23-26, 2023*. 113–129. doi:10.1145/3600006.3613172
- [45] Simon Spies, Lennard Gäher, Joseph Tassarotti, Ralf Jung, Robbert Krebbers, Lars Birkedal, and Derek Dreyer. 2022. Later credits: resourceful reasoning for the later modality. *Proc. ACM Program. Lang.* 6, ICFP, Article 100 (Aug. 2022), 29 pages. doi:10.1145/3547631
- [46] Nikhil Swamy, Joel Weinberger, Cole Schlesinger, Juan Chen, and Benjamin Livshits. 2013. Verifying higher-order programs with the dijkstra monad. In *ACM SIGPLAN Conference on Programming Language Design and Implementation, PLDI ’13, Seattle, WA, USA, June 16-19, 2013*. 387–398. doi:10.1145/2491956.2491978
- [47] Joseph Tassarotti, Tej Chajed, and Perennial contributors. 2021. Crash Borrow in Perennial’s Mechanization. [https://github.com/mit-pdos/perennial/blob/c4c806b6ede0580dc8cb72ca873d25e3fb7e564d/src/goose\\_lang/crash\\_modality.v](https://github.com/mit-pdos/perennial/blob/c4c806b6ede0580dc8cb72ca873d25e3fb7e564d/src/goose_lang/crash_modality.v)
- [48] Joseph Tassarotti and Robert Harper. 2019. A separation logic for concurrent randomized programs. *Proc. ACM Program. Lang.* 3, POPL (2019), 64:1–64:30. doi:10.1145/3290377
- [49] Joseph Tassarotti and Perennial contributors. 2022. Crash Borrow in Perennial’s Mechanization. [https://github.com/mit-pdos/perennial/blob/5189a4ecc8583a7042ccccbb3bb13cfb57e4c5/src/goose\\_lang/crash\\_borrow.v](https://github.com/mit-pdos/perennial/blob/5189a4ecc8583a7042ccccbb3bb13cfb57e4c5/src/goose_lang/crash_borrow.v)
- [50] Amin Timany, Robbert Krebbers, Derek Dreyer, and Lars Birkedal. 2024. A Logical Approach to Type Soundness. *J. ACM* 71, 6 (2024), 40:1–40:75. doi:10.1145/3676954
- [51] Aaron Turon, Derek Dreyer, and Lars Birkedal. 2013. Unifying refinement and hoare-style reasoning in a logic for higher-order concurrency. In *ACM SIGPLAN International Conference on Functional Programming, ICFP’13, Boston, MA, USA - September 25 - 27, 2013*. 377–390. doi:10.1145/2500365.2500600
- [52] Aaron Turon, Viktor Vafeiadis, and Derek Dreyer. 2014. GPS: navigating weak memory with ghosts, protocols, and separation. In *Proceedings of the 2014 ACM International Conference on Object Oriented Programming Systems Languages & Applications, OOPSLA 2014, part of SPLASH 2014, Portland, OR, USA, October 20-24, 2014*. 691–707. doi:10.1145/2660193.2660243
- [53] Viktor Vafeiadis and Chinmay Narayan. 2013. Relaxed separation logic: a program logic for C11 concurrency. In *Proceedings of the 2013 ACM SIGPLAN International Conference on Object Oriented Programming Systems Languages & Applications, OOPSLA 2013, part of SPLASH 2013, Indianapolis, IN, USA, October 26-31, 2013*. 867–884. doi:10.1145/2509136.2509532
- [54] Orpheas van Rooij and Robbert Krebbers. 2025. Affect: An Affine Type and Effect System. *Proc. ACM Program. Lang.* 9, POPL, Article 5 (Jan. 2025), 29 pages. doi:10.1145/3704841
- [55] Simon Friis Vindum, Aina Linn Georges, and Lars Birkedal. 2025. The Nextgen Modality: A Modality for Non-Frame-Preserving Updates in Separation Logic. In *Proceedings of the 14th ACM SIGPLAN International Conference on Certified Programs and Proofs (Denver, CO, USA) (CPP ’25)*. Association for Computing Machinery, New York, NY, USA, 83–97. doi:10.1145/3703595.3705876
- [56] Max Vistrup, Michael Sammler, and Ralf Jung. 2025. Program Logics à la Carte. *Proc. ACM Program. Lang.* 9, POPL (2025), 300–331. doi:10.1145/3704847

- [57] James R. Wilcox, Ilya Sergey, and Zachary Tatlock. 2017. Programming Language Abstractions for Modularly Verified Distributed Systems. In *2nd Summit on Advances in Programming Languages, SNAPL 2017, May 7-10, 2017, Asilomar, CA, USA*. 19:1–19:12. doi:[10.4230/LIPICS.SNAPL.2017.19](https://doi.org/10.4230/LIPICS.SNAPL.2017.19)
- [58] James R. Wilcox, Doug Woos, Pavel Panchekha, Zachary Tatlock, Xi Wang, Michael D. Ernst, and Thomas E. Anderson. 2015. Verdi: a framework for implementing and formally verifying distributed systems. In *Proceedings of the 36th ACM SIGPLAN Conference on Programming Language Design and Implementation, Portland, OR, USA, June 15-17, 2015*. 357–368. doi:[10.1145/2737924.2737958](https://doi.org/10.1145/2737924.2737958)
- [59] Li-yao Xia, Yannick Zakowski, Paul He, Chung-Kil Hur, Gregory Malecha, Benjamin C. Pierce, and Steve Zdancewic. 2020. Interaction trees: representing recursive and impure programs in Coq. *Proc. ACM Program. Lang.* 4, POPL (2020), 51:1–51:32. doi:[10.1145/3371119](https://doi.org/10.1145/3371119)

## A Ficus

### A.1 Semantics

**Head reduction rules.**  $e \xrightarrow{\vec{\kappa}}_h e'$

HD-BETA	$(\text{rec } f \ x. e) \ v \xrightarrow{\varepsilon}_h e[(\text{rec } f \ x. e), v/f, x]$	HD-CONT	$(\text{cont } N) \ v \xrightarrow{\varepsilon}_h N[v]$
HD-EFFAPR	$e_1 \ \$ (N)[v_2] \xrightarrow{\varepsilon}_h \$ (e_1 \ N)[v_2]$	HD-DO	$\text{do } v \xrightarrow{\varepsilon}_h \$ ([\ ])[v]$
HD-EFFAPL	$\$(N)[v_1] \ v_2 \xrightarrow{\varepsilon}_h \$ (N \ v_2)[v_1]$	HD-PICK	$\text{pick} \xrightarrow{\varepsilon}_h z$
HD-EFFDO	$\text{do } \$ (N)[v] \xrightarrow{\varepsilon}_h \$ (\text{do } N)[v]$	HD-OBS	$\text{observe } v \xrightarrow{[v]}_h ()$
HD-TRYEFF	$\text{try } \$ (N)[v_0] \ \text{with } v_1 \ k \Rightarrow e_1 \mid \text{ret } v_2 \Rightarrow e_2 \xrightarrow{\varepsilon}_h e_1[v_0, \text{cont } N/v_1, k]$		
HD-TRYVAL	$\text{try } v_0 \ \text{with } v_1 \ k \Rightarrow e_1 \mid \text{ret } v_2 \Rightarrow e_2 \xrightarrow{\varepsilon}_h e_2[v_0/v_2]$		

**Pure Reduction and Its Reflexive Transitive Closure.**  $e \xrightarrow{\vec{\kappa}} e'$  and  $e \xrightarrow{\vec{\kappa}}^* e'$

$$e \xrightarrow{\vec{\kappa}} e' \triangleq \exists K, \tilde{e}, \tilde{e}'. e = K[\tilde{e}] \wedge e' = K[\tilde{e}'] \wedge \tilde{e} \xrightarrow{\vec{\kappa}}_h \tilde{e}'$$

$$e \xrightarrow{\vec{\kappa}}^* e' \triangleq (e = e' \wedge \vec{\kappa} = \varepsilon) \vee (\exists e'', \vec{\kappa}_1, \vec{\kappa}_2. \vec{\kappa} = \vec{\kappa}_1 \uplus \vec{\kappa}_2 \wedge e \xrightarrow{\vec{\kappa}_1}_h e'' \wedge e'' \xrightarrow{\vec{\kappa}_2}^* e')$$

### A.2 Reasoning Rules

$\frac{\text{EWP-VALUE} \quad w_1 \models_{w_2} \Phi(v)}{\text{ewp}_{w_1, w_2} v \langle \Psi \rangle \{ \Phi \}}$	$\frac{\text{EWP-DO} \quad \Psi(v, \Phi)}{\text{ewp}_w \text{do } v \langle \Psi \rangle \{ \Phi \}}$	$\frac{\text{EWP-DOFUPD} \quad w_1 \models_{\perp} \Psi(v, (\lambda r. \perp \models_{w_2} \Phi(r)))}{\text{ewp}_{w_1, w_2} \text{do } v \langle \Psi \rangle \{ \Phi \}}$
$\frac{\text{EWP-MONO} \quad \text{ewp}_{w_1, w_2} e \langle \Psi \rangle \{ \Phi \} \quad \Psi \sqsubseteq \Psi' \quad \forall v. \Phi(v) \multimap \models_{w_2} \Phi'(v)}{\text{ewp}_{w_1, w_2} e \langle \Psi' \rangle \{ \Phi' \}}$	$\frac{\text{EWP-WORLDMONO} \quad \text{ewp}_w e \langle \Psi \rangle \{ \Phi \} \quad W \sqsubseteq W'}{\text{ewp}_{w', e} \langle \Psi \rangle \{ \Phi \}}$	
$\frac{\text{EWP-FRAME} \quad R \quad \text{ewp}_{w_1, w_2} e \langle \Psi \rangle \{ \Phi \}}{\text{ewp}_{w_1, w_2} e \langle \Psi \rangle \{ v. R * \Phi(v) \}}$	$\frac{\text{EWP-WUPDPRE} \quad w_1 \models_{w_2} \text{ewp}_{w_2, w_3} e \langle \Psi \rangle \{ \Phi \}}{\text{ewp}_{w_1, w_3} e \langle \Psi \rangle \{ \Phi \}}$	
$\frac{\text{EWP-WUPDPOST} \quad \text{ewp}_{w_1, w_2} e \langle \Psi \rangle \{ v. w_2 \models_{w_3} \Phi(v) \}}{\text{ewp}_{w_1, w_3} e \langle \Psi \rangle \{ \Phi \}}$	$\frac{\text{EWP-PURE} \quad \text{ewp}_{w_1, w_2} e \langle \Psi \rangle \{ \Phi \} \quad e' \xrightarrow{*} e}{\text{ewp}_{w_1, w_2} e' \langle \Psi \rangle \{ \Phi \}}$	
$\frac{\text{EWP-BIND} \quad \text{ewp}_{w_1, w_2} e \langle \Psi \rangle \{ v. \text{ewp}_{w_2, w_3} N[v] \langle \Psi \rangle \{ \Phi \} \}}{\text{ewp}_{w_1, w_3} N[e] \langle \Psi \rangle \{ \Phi \}}$	$\frac{\text{EWP-WORLDFRAME} \quad \text{ewp}_{w_1, w_2} e \langle \Psi \rangle \{ \Phi \}}{\text{ewp}_{w_1 \oplus w, w_2 \oplus w} e \langle \Psi \rangle \{ \Phi \}}$	
$\frac{\text{EWP-OBS} \quad \text{primProph}(\vec{v})}{\text{ewp}_w \text{observe } w \langle \Psi \rangle \{ \_ . \exists \vec{v}'. \vec{v} = w :: \vec{v}' * \text{primProph}(\vec{v}') \}}$		

### A.3 Model

$$\begin{aligned} \text{ewp}_{w_1, w_2} v \langle \Psi \rangle \{ \Phi \} &\triangleq w_1 \models_{w_2} \Phi(v) \\ \text{ewp}_{w_1, w_2} \$ (N)[v] \langle \Psi \rangle \{ \Phi \} &\triangleq w_1 \models_{\perp} \Psi(v, \lambda w. \models_{\perp} \triangleright (\text{ewp}_{\perp, w_2} N[w] \langle \Psi \rangle \{ \Phi \})) \\ \text{ewp}_{w_1, w_2} e \langle \Psi \rangle \{ \Phi \} &\triangleq \forall \vec{\kappa}_1, \vec{\kappa}_2. \text{primProph}^*(\vec{\kappa}_1 \uplus \vec{\kappa}_2) \multimap w_1 \models_{\perp} (\exists e'. e \rightarrow e') * \end{aligned}$$

$$\forall e'. e \xrightarrow{\vec{\kappa}_1} e' \multimap \models_{\perp} \triangleright \models_{\perp} \text{primProph}^{\bullet}(\vec{\kappa}_2) * \text{ewp}_{\perp, W_2} e' \langle \Psi \rangle \{ \Phi \}$$

$$\text{primProph}^{\bullet}(\vec{v}) \triangleq [\![ \bullet \overline{Ex}(\vec{v}) ]\!]^{Y_P} \quad \text{primProph}(\vec{v}) \triangleq [\![ \circ \overline{Ex}(\vec{v}) ]\!]^{Y_P}$$

## B Prophetic Heap

$$\text{PROPH\_ALLOC}(v, \Phi) \triangleq \exists x. v = (\text{alloc}, x) * (\forall l, \vec{v}. l \mapsto x * \text{proph}(l, \vec{v}) \text{prophE}(l) \multimap \Phi(l))$$

$$\begin{aligned} \text{PROPH\_LOAD}(v, \Phi) &\triangleq \exists l, x, \vec{v}. v = (\text{load}, l) * l \mapsto x * \text{proph}(l, \vec{v}) * \text{prophE}(l) * \\ &(\forall \vec{v}'. l \mapsto x * v = (x, \text{load}) :: \vec{v}' * \text{proph}(l, \vec{v}') * \text{prophE}(l) \multimap \Phi(x)) \end{aligned}$$

$$\text{PROPH\_LOAD}'(v, \Phi) \triangleq \exists l, q, x. v = (\text{load}, l) * l \xrightarrow{q} x * \text{prophD}(l) * (l \xrightarrow{q} x \multimap \Phi(x))$$

$$\begin{aligned} \text{PROPH\_STORE}(v, \Phi) &\triangleq \exists l, x, y, \vec{v}. v = (\text{store}, (l, y)) * l \mapsto x * \text{proph}(l, \vec{v}) * \text{prophE}(l) * \\ &(\forall \vec{v}'. l \mapsto y * \vec{v} = ((), \text{store}(y)) :: \vec{v}' * \text{proph}(l, \vec{v}') * \text{prophE}(l) \multimap \Phi(())) \end{aligned}$$

$$\begin{aligned} \text{PROPH\_CAS}(v, \Phi) &\triangleq \exists l, w, x, y, \vec{v}. v = (\text{cas}, (l, x, y)) * l \mapsto w * \text{proph}(l, \vec{v}) * \text{prophE}(l) * \\ &(\forall \vec{v}'. l \mapsto (\text{if } w = x \text{ then } y \text{ else } w) * \vec{v} = (w = x, \text{cas}(x, y)) :: \vec{v}' * \\ &\text{proph}(l, \vec{v}') * \text{prophE}(l) \multimap \Phi(w, w = x)) \end{aligned}$$

$$\begin{aligned} \text{PROPH\_CAS}'(v, \Phi) &\triangleq \exists l, q, w, x, y. v = (\text{cas}, (l, x, y)) * l \xrightarrow{q} w * w \neq x * \text{prophD}(l) * \\ &(l \xrightarrow{q} w \multimap \Phi(w, \text{false})) \end{aligned}$$

## C Crash Recovery System

This section uses the complete  $\text{Tok}_C(\mathcal{E}, R_c)$  token. Compared to  $\text{Tok}_C(R_c)$  used in §6, it has one extra parameter for the enabled crash borrows. The connection between two tokens is  $\text{Tok}_C(\mathcal{T}, R_c) = \text{Tok}_C(\mathcal{T}, R_c)$ .

### C.1 Post-crash Modality

$$\begin{aligned} \text{crashed} &\triangleq \exists M: \text{PID} \rightarrow \text{List}(\text{Val} \times \text{Val}). [\![ \overline{M} ]\!]^{Y_{\text{proph}}} * \forall p \mapsto \vec{v} \in M. \vec{v} = \varepsilon \\ \blacklozenge P &\triangleq \text{crashed} \multimap \text{crashed} * P \end{aligned}$$

### C.2 Protocol

$\mathcal{R}$  is the global crash invariant. There is no requirement on  $\Phi$  because crash will never return. CRASHCONC intentionally uses the same tag as the regular CONC protocol to prevent having two schedulers. As said by the FORK' protocol, a child thread is permitted to change the crash condition during execution, as long as it is consistent with the  $\text{Tok}_C(\mathcal{E}, R)$  token. The crash condition cannot be violated even if a thread terminates.

$$\begin{aligned} \text{CRASH}(v, \Phi) &\triangleq v = (\text{crash}, ()) * (\models \mathcal{R}) \\ \text{DURA}_W(\Psi)(v, \Phi) &\triangleq \exists R. \Psi(v, (\lambda r. \perp \models_{W \oplus \text{Tok}_C(\mathcal{T}, R)} (R \wedge_{W \oplus \text{Tok}_C(\mathcal{T}, R)} \models_{\perp} \Phi(r)))) \\ \text{CRASHCONC}_W(\Psi) &\triangleq \text{DURA}_W(\Psi \oplus \text{FORK}'_W(\Psi)) \\ \text{FORK}'_W(\Psi)(v, \Phi) &\triangleq \exists e. v = (\text{fork}, \lambda_. e) * \\ &\triangleright \text{ewp}_{W \oplus \text{Tok}_C(\mathcal{T}, \text{True}), \perp} e \langle \text{CRASHCONC}_W(\Psi) \rangle \left\{ \exists R_e. \perp \models_{W \oplus \text{Tok}_C(\mathcal{T}, R_e)} R_e \right\} * \Phi(()) \end{aligned}$$



Interaction between protocols:

$$\begin{array}{c}
\text{DURA}_W(\Psi) \sqsubseteq \text{ATOM}_W(\Psi) \preceq \Psi \qquad \text{ATOM}_W(\Psi) \sqsubseteq \text{CONC}_W(\Psi) \\
\\
\text{DURA}_W(\Psi) \sqsubseteq \text{CRASHCONC}_W(\Psi) \\
\\
\frac{\text{EWP-SEQ-ATOM} \quad \text{ewp}_{W_I} e \langle \Psi \rangle \{ \Phi \}}{\text{ewp}_{W_I \oplus W} e \langle \text{ATOM}_W(\Psi) \rangle \{ \Phi \}} \qquad \frac{\text{EWP-ATOM-DURA} \quad \Box(R' \multimap R) \quad R' \quad \text{ewp}_W e \langle \text{ATOM}_W(\Phi) \rangle \{ v. R' \multimap \Phi(v) \}}{\text{ewp}_{W \oplus \text{Tok}_C(\tau, R)} e \langle \text{DURA}_W(\Phi) \rangle \{ \Phi \}} \\
\\
\frac{\text{EWP-CONC-CRASHCONC} \quad \text{ewp}_W e \langle \text{CONC}_W(\Phi) \rangle \{ \Phi \}}{\text{ewp}_{W \oplus \text{Tok}_C(\tau, \text{True})} e \langle \text{CRASHCONC}_W(\Phi) \rangle \{ \Phi \}}
\end{array}$$

### C.3 Crash Hoare Logic

$$\begin{aligned}
\text{ewpc}_{(\mathcal{E}_1, R_1), (\mathcal{E}_2, R_2)}^0 e \langle \Psi \rangle \{ \Phi \} &\triangleq \text{ewp}_{W_1, W_2} e \langle \text{CRASHCONC}_{\text{Tok}_I(\tau)}(\Psi) \rangle \{ \Phi \} \\
\text{where } W_i &\triangleq \text{Tok}_I(\mathcal{E}_i) \oplus \text{Tok}_C(\mathcal{E}_i, R_i)
\end{aligned}$$

The ewpc assertion does not support the monotonic rule, but this will not become a restriction in practice because one can always use the upward closure of a non-mask-changing ewpc.

$$\text{ewpc}_{(\mathcal{E}, R)} e \langle \Psi \rangle \{ \Phi \} \triangleq \forall R', \Phi'. (\forall v. \Phi(v) \multimap \Phi'(v)) \wedge (R \multimap R') \multimap \text{ewpc}_{(\mathcal{E}, R')}^0 e \langle \Psi \rangle \{ \Phi' \}$$

The logical conjunction  $\wedge$  between  $\Phi$  and  $R$  precisely captures the monotonicity in a crash system. Only one part of this conjunction is needed at a time. The  $\Phi$  part is used during normal execution and the  $R$  part is used when the system crashes.

### C.4 Crash Borrow

See [Figures 7 and 8](#).

### C.5 Asynchronous Disk

The client rules about the asynchronous disk are specified by protocols in [Figure 9a](#). Resource  $l \mapsto^d [v_c]v$  declares the ownership of an asynchronous disk location  $l$ . There are two values associated with one location:  $v$  is the value visible to the system before crash, and  $v_c$  is the value visible to the system after crash. Using the post-crash modality, this means  $l \mapsto^d [v_c]v \vdash \blacklozenge l \mapsto^d v_c$ . Notice that because asynchronous disk is essentially a synchronous disk plus a software buffer, the points-to assertion will become a regular disk points-to  $l \mapsto^d v_c$  after crash. Only at the recovery stage will the asynchronous disk points-to assertion be recreated:  $l \mapsto^d v_c \vdash \blacklozenge l \mapsto^d [v_c]v_c$ , where  $\blacklozenge$  is called *setup modality*.

The ADISK\_LOAD protocol is standard. According to protocol ADISK\_STORE, an `adisk_store` effect immediately updates the before-crash value at location  $l$  to  $w$ , but the after-crash value  $v'_c$  could be either  $w$  or  $v_c$  depending on whether the buffer will be written back before the next crash. An `adisk_barrier` effect issues a global write barrier that writes-back the *whole* buffer to the disk. Therefore, the client can use this effect to write-back an arbitrary number of locations. Because the buffer was indeed written back before crash, we now know that  $v_c$  must have equaled to  $v$ .

**Use of Prophecy Variables in the Effect Handler.** The handler for asynchronous disk is shown in [Figure 9b](#) and important logic constructions used to verify it are listed in [Figure 9c](#). It uses a volatile state to store the buffer. For each buffered location, the handler associates a prophecy variable to it, indicating whether this location will be written back before crash. For buffer item  $l \mapsto (v, p)$ , the value  $v$  will be written back iff  $\exists \vec{v}'. \text{proph}(p, ((), \text{true}) :: \vec{v}')$ , which is formalized

$$\begin{array}{c}
\text{WUPD-CBRWALLOC} \quad \frac{\triangleright P \quad \square(\triangleright P \multimap \triangleright R)}{\text{Tok}_C(\mathcal{E}, R_c * R) \models_{\text{Tok}_C(\mathcal{E}, R_c)} \boxed{P \mid R}^N} \quad
\text{WUPD-CBRWRETURN} \quad \frac{\boxed{P \mid R}^N \quad N \subseteq \mathcal{E}}{\text{Tok}_C(\mathcal{E}, R_c) \models_{\text{Tok}_C(\mathcal{E}, R_c * R)} \triangleright P} \quad
\text{WUPD-CBRWRENAME} \quad \frac{\boxed{P \mid R}^N \quad N \subseteq \mathcal{E}}{\models_{\text{Tok}_C(\mathcal{E}, R_c)} \boxed{P \mid R}^{N'}} \\
\\
\text{WUPD-CBRWACCUPDATE} \quad \frac{\boxed{P \mid R}^N \quad N \subseteq \mathcal{E}}{\text{Tok}_C(\mathcal{E}, R_c) \models_{\text{Tok}_C(\mathcal{E} \setminus N, R_c)} \triangleright P * \left( \forall Q. \triangleright Q * \square(\triangleright Q \multimap \triangleright R) \multimap \text{Tok}_C(\mathcal{E} \setminus N, R_c) \models_{\text{Tok}_C(\mathcal{E}, R_c)} \boxed{Q \mid R}^N \right)} \\
\\
\text{WUPD-CBRWMONO} \quad \frac{\boxed{P \mid R}^N \quad \triangleright \square(P' \multimap R') \quad \triangleright (P \multimap P') \quad \triangleright \square(R' \multimap R) \quad N \subseteq \mathcal{E}}{\models_{\text{Tok}_C(\mathcal{E}, R_c)} \boxed{P' \mid R'}^N} \\
\\
\text{WUPD-CBRWSPLIT} \quad \frac{\boxed{P_1 * P_2 \mid R_1 * R_2}^N \quad \square(\triangleright P_1 \multimap \triangleright R_1) \quad \square(\triangleright P_2 \multimap \triangleright R_2) \quad N \subseteq \mathcal{E}}{\models_{\text{Tok}_C(\mathcal{E}, R_c)} \boxed{P_1 \mid R_1}^N * \boxed{P_2 \mid R_2}^N} \\
\\
\text{WUPD-CBRWCOMBINE} \quad \frac{\boxed{P_1 \mid R_1}^N \quad \boxed{P_2 \mid R_2}^N \quad N \subseteq \mathcal{E}}{\models_{\text{Tok}_C(\mathcal{E}, R_c)} \boxed{P_1 * P_2 \mid R_1 * R_2}^N}
\end{array}$$

(a) Client rules.

WSAT-CINVALLOC

$$\models \exists \gamma_{brw}, \gamma_{cinv}, \gamma_{cinvset}, \gamma_{cond}, \gamma_{active}. \text{Tok}_C(\top, \mathcal{R}) * [\circ \text{Ex}(\{\{l\}\})]^{Y_{cinvset}} * [\bullet \text{Ex}(l)]^{Y_{active}} * \text{CInv}(\mathcal{R})$$

WSAT-CINVDESTRUCT

$$\frac{[\circ \text{Ex}(\text{dom}(I))]^{Y_{cinvset}} \quad \bigstar_{l \mapsto R_c \in I} [\circ \{l \mapsto \text{Ag}(\triangleright R_c)\}]^{Y_{cond}} * \triangleright R_c \quad \text{CInv}(\mathcal{R}) \quad \text{WCBrw} \quad [\top]^E}{\models \triangleright \mathcal{R}}$$

(b) Handler rules.

Fig. 7. Reasoning rules of crash borrows and crash conditions.

by the willWB assertion. For a **adisk\_load** effect, the handler returns the cached value if location  $l$  is in the buffer (line 4), otherwise, it uses **disk\_load** operation to load the value directly from the physical disk and buffers the result (line 5). For a **adisk\_store** effect, the handler always writes the result to the buffer, but if the location is already in the buffer, the handler will resolve the associated prophecy variable to **false**, meaning that the old value in the buffer will never be written back (as it has been overwritten by the new value). For a **barrier** effect, the handler writes back the whole buffer and resolves each prophecy variable to **true**. In the event of a crash, all prophecy variables will become  $\varepsilon$  and because  $\varepsilon \neq ()$ , **true**  $:: \_$ , we learn that remaining values in the buffer will never be written back.

Concretely, the asynchronous disk points-to assertion  $l \mapsto^d [v_c]v$  is defined as a view of the  $\text{adp}(B, l, v_c, v)$  assertion, which  $B$  is the buffer. The assertions consists of two cases. If  $l$  is not in the buffer, then  $v$  is in the physical disk and  $v_c = v$ . If  $l$  is in the buffer, then  $v$  is the buffered value and

$$\begin{aligned}
\boxed{P \mid R}^N &\triangleq \exists i. i \in \mathcal{N} * \left[ \circ \{ i \mapsto \text{Ag}(\blacktriangleright(P, R)) \} \right]^{Y_{brw}} & \text{CInv}(\mathcal{R}) &\triangleq \left[ \circ \text{Ex}(\blacktriangleright \mathcal{R}) \right]^{Y_{cinv}} \\
\text{Tok}_C(\mathcal{E})R_c &\triangleq \text{WCBrw} \oplus \left[ \mathcal{E} \right]^E \oplus \left( \exists l. \left[ \circ \text{Ex}(l) \right]^{Y_{active}} * \left[ \circ \{ l \mapsto \text{Ag}(\blacktriangleright R_c) \} \right]^{Y_{cond}} \right) \\
\text{WCBrw} &\triangleq \exists B: \mathbb{N} \xrightarrow{\text{fin}} i\text{Prop} \times i\text{Prop}, C: \mathbb{N} \xrightarrow{\text{fin}} i\text{Prop}, \mathcal{R}: i\text{Prop}. \\
&\left[ \bullet \text{ag}(\text{next}(B)) \right]^{Y_{brw}} * \left[ \bullet \text{ag}(\text{next}(C)) \right]^{Y_{cond}} * \left[ \bullet \text{Ex}(\text{dom}(C)) \right]^{Y_{cinvset}} * \left[ \bullet \text{Ex}(\blacktriangleright \mathcal{R}) \right]^{Y_{cinv}} * \\
&\left( \bigstar_{i \mapsto (P, R) \in B} \left( \blacktriangleright P * \left[ \{ i \} \right]^D \vee \left[ \{ i \} \right]^E \right) * \square(\blacktriangleright P \multimap \blacktriangleright R) \right) * \left( \left( \bigstar_{(\_R) \in B} \blacktriangleright R \right) * \left( \bigstar_{R_c \in C} \blacktriangleright R_c \right) \multimap \blacktriangleright \mathcal{R} \right)
\end{aligned}$$

Fig. 8. Model of crash borrows and crash conditions.

the value in the physical disk depends on willWB. If willWB, then the current value in the physical disk is unknown but also unimportant because it will eventually become  $v$ ; otherwise, the value in the physical disk is  $v_c$ . The connection between  $\text{adp}(B, l, v_c, v)$  and  $l \mapsto^d [v_c]v$  is enforced by the authoritative resource algebra.

#### D Distributed System

$$\begin{aligned}
\text{specn}_t^Y(e) &\triangleq \exists k, r. \left[ \{ k, r, t \} \right]^Y * \text{loop}(k \ r, e) \\
\text{loop} &\triangleq \text{lfp } \text{loop} \ (e_0, e). (\forall K. \text{spec}(K[e_0]) \multimap \models_{\perp} \text{spec}(K[e])) \\
&\vee (\forall K. \text{spec}(K[e_0]) \multimap \exists H, t, M. \text{spec}(K[\text{eff}(\text{recv}, t) \ H]) * t \rightsquigarrow^{\text{lb}} M * \text{loop}(H[\text{inl}()], e)) \\
&\vee (\forall K. \text{spec}(K[e_0]) \multimap \exists H, s, t, m, M. \text{spec}(K[\text{eff}(\text{recv}, t) \ H]) * t \rightsquigarrow^{\text{lb}} M * (s, t, m) \in M \\
&\quad * \text{loop}(H[\text{inr}(s, t, m)], e))
\end{aligned}$$

It also uses the evidence accumulation technique described in §4.3, but for a given thread, in addition to accumulating the evidence it can be executed to  $e$  via pure steps, we now also need to accumulate the evidence that the thread may raise **recv** effects later on while executing to  $e$ . Intuitively, the point is that if a **recv** executed at some point  $x$  can return a message  $m$ , then if we delay that **recv** to some later time  $x'$ , it is still possible for it to return that same message  $m$ . This is because the set of messages is monotonically growing. This evidence that a later **recv** can return a given message is accumulated in the least fixed point loop. Intuitively,  $\text{loop}(e_0, e)$  allows  $e_0$  to execute to  $e$  via three ways: (1) Some pure steps. (2) First raising a **recv** effect that receives nothing and then continuing with the result of **recv**. (3) First raising a **recv** effect that receives some messages  $(s, t, m)$  and then continuing with the result of **recv**. Assertion  $t \rightsquigarrow^{\text{lb}} M$  in cases (2) and (3) is a lower-bound resource of  $t \rightsquigarrow \cdot$ , meaning that  $M$  is a subset of messages that have ever been sent to address  $t$ .

$$\begin{aligned}
\text{ADISK\_LOAD}(u, \Phi) &\triangleq \exists l, v_c, v. u = (\text{adisk\_load}, l) * l \mapsto^d [v_c]v * (l \mapsto^d [v_c]v \multimap \Phi(v)) \\
\text{ADISK\_STORE}(u, \Phi) &\triangleq \exists l, v_c, v, w. u = (\text{adisk\_store}, (l, w)) * l \mapsto^d [v_c]v * \\
&\quad (\forall v'_c \in \{w, v'_c\}. l \mapsto^d [v'_c]w \multimap \Phi(())) \\
\text{BARRIER}(u, \Phi) &\triangleq \exists M. u = (\text{barrier}, ()) * \left( \bigstar_{l \mapsto (v_c, v) \in m} l \mapsto^d [v_c]v \right) * \\
&\quad \left( \left( \bigstar_{l \mapsto (v_c, v) \in m} v_c = v * l \mapsto^d [v_c]v \right) \multimap \Phi(()) \right)
\end{aligned}$$

(a) Protocol.

```

runadisk  $\triangleq$   $\lambda$ main.
1  write  $\emptyset$ ;
2  try main () with ret  $v \Rightarrow v \mid v \ k \Rightarrow$  match  $v$  with
3    (adisk_load,  $l$ )  $\Rightarrow$  let  $buf :=$  read in
4      (if  $l \in buf$  then  $k$  (fst  $buf[l]$ )
5       else let  $p :=$  newproph,  $v :=$  disk_load  $l$  in write ( $[l \mapsto (v, p)]buf$ );  $k$  ( $v$ ))
6  | (adisk_store, ( $l, w$ ))  $\Rightarrow$  let  $buf :=$  read in
7    (if  $l \in buf$  then resolve_proph (snd  $buf[l]$ ) to false;
8     let  $p :=$  newproph in write ( $[l \mapsto (w, p)]buf$ );  $k$  ())
9  | (barrier, ())  $\Rightarrow$  let  $buf :=$  read in
10   iter ( $\lambda l$  ( $v, p$ ). resolve_proph  $p$  to true; disk_store  $l$   $v$ )  $buf$ ; write  $\emptyset$ 
11 | ( $\eta, v$ )  $\Rightarrow$  do ( $\eta, v$ )

```

(b) Implementation.

$$\begin{aligned}
\text{willWB}(\vec{v}) &\triangleq \exists \vec{v}'. \vec{v} = ((), \text{true}) :: \vec{v}' & \text{adp}(B, l, v_c, v) &\triangleq l \notin B * l \mapsto^d v * v_c = v \vee \\
\exists p. B[l] &= (v, p) * \exists \vec{v}. \text{proph}(p, \vec{v}) * (\text{if } \text{willWB}(\vec{v}) \text{ then } (\exists x. l \mapsto^d x) * v_c = v \text{ else } l \mapsto^d v_c)
\end{aligned}$$

(c) Verification.

Fig. 9. Asynchronous Disk.