

# Trillium: Intensional Refinement in Higher-Order Separation Logic

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joint work with Amin Timany<sup>1</sup>, Léo Stefanesco<sup>3</sup>, Jonas Kastberg Hinrichsen<sup>1</sup>, Léon Gondelman<sup>2</sup>, Abel Nieto<sup>1</sup>, and Lars Birkedal<sup>1</sup>.

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#### Implementations

Models



How do we connect realistic implementations to more abstract models?

- Fork-based (node-local) concurrency
- Socket-based communication with serialization
- Higher-order functions, higher-order state, ...

### **This work**

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### This work

**Trillium** A higher-order separation logic framework for showing different notions of trace refinement between programs and models.

We consider two instantiations of the framework:

- **Aneris** for reasoning about safety properties of implementations of distributed systems communicating over an unreliable network.
  - **Fairis** for reasoning about termination of fine-grained concurrent programs under fair scheduling assumptions.

A language-generic framework for showing **lockstep simulation**, built on top of the Iris separation logic framework and mechanized in the Coq proof assistant.

 $\delta_1$  $\langle$  $e_1$ 







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We will weaken lockstep simulation through model constructions.

# **Key Ideas**

- **1.** Use a program logic  $\{P\} e \{Q\}$  to reason about the program.
- **2.** Use a separation logic resource  $Model(\delta)$  to embed the current model state in the logic and restrict its progression to preserve properties of interest.
- **3.** Encode the refinement mapping using Iris invariant assertions P.



### Example

To show that  $e \triangleq$  while true do  $\ell \leftarrow !\ell + 1$  end refines the state-transition system

$$\bigcirc \longrightarrow 1 \longrightarrow 2 \longrightarrow 3 \longrightarrow \cdots$$

one shows a specification of the shape

$$[\exists n. \ell \mapsto n * \mathsf{Model}(n)] e \{Q\}.$$

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But lockstep simulation-while sound-is much too restrictive, e.g.,

 $\delta \rightharpoonup \delta'$ 

 $\overline{\{\mathsf{Model}(\delta)\}\,n+m\,\{v.\,v=(n+m)*\mathsf{Model}(\delta')\}}$ 

# **Safety Properties**

Models are (often) simpler than implementations so stuttering is necessary. To preserve safety properties, it is sound to allow **unrestricted stuttering**.



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## Aneris

We "bake in" the reflexive closure, instantiate Trillium with AnerisLang–an ML-like language with UDP communication primitives–and recover *Aneris* [ESOP'20], a **distributed separation logic**.

All existing specifications and reasoning principles still hold, with the addition of just **one rule** for progressing the model.

 $\frac{\{P\} \ e \ \{Q\} \qquad \delta \rightharpoonup \delta' \qquad \mathsf{Atomic}(e)}{\{P * \mathsf{Model}(\delta)\} \ e \ \{Q * \mathsf{Model}(\delta')\}}$ 

### **Single-Decree Paxos by Refinement**



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#### **Theorem (Consistency)**

If  $\delta_{\text{init}} \rightharpoonup^* \delta'_{\text{SDP}}$  and both  $\text{Chosen}(\delta'_{\text{SDP}}, v_1)$  and  $\text{Chosen}(\delta'_{\text{SDP}}, v_2)$  then  $v_1 = v_2$ .

$$\{ \underline{I_{SDP}} * \dots \} \text{ acceptor } L \ a \{ \dots \}$$
$$\{ \underline{I_{SDP}} * \dots \} \text{ proposer } A \ s \ b \ v \{ \dots \}$$
$$\{ \underline{I_{SDP}} * \dots \} \text{ learner } s \ a \{ \dots \}$$

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where

 $I_{\text{SDP}} \triangleq \exists \delta_{\text{SDP}}. \operatorname{Model}(\delta_{\text{SDP}}) * \operatorname{PaxosRes}_{\bullet}(\delta_{\text{SDP}}) * \operatorname{BallotCoh}(\delta_{\text{SDP}})$ 

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$$\{ \underline{I_{SDP}} * (PaxosRes_{\circ}(...)) * ... \} \text{ acceptor } L \ a \{ ... \}$$
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$$\{ \underline{I_{SDP}} * PaxosRes_{\circ} (...) * ... \} acceptor L a \{...\}$$

$$\{ \underline{I_{SDP}} * pending(b) * ... \} proposer A s b v \{...\}$$

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Takeaway: the invariant is quite simple and only concerned with refinement!

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Putting everything together gives us consistency for all program traces.

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- **1.** No need to come up with a new consensus proof.
- 2. The refinement proof requires almost no advanced ghost state usage.
- **3.** As the model is embedded as a resource in the logic, we can **internalize** properties of the model while proving specifications

 $\frac{\{P*v_1=v_2\} e \{Q\}}{\{P*\mathsf{Chosen}(v_1)*\mathsf{Chosen}(v_2)\} e \{Q\}}$ 

which allows us to verify clients, e.g.,

```
let client addr =
   // ...
   let v1 = client_deser m1 in
   let v2 = client_deser m2 in
   assert (v1 == v2); v1.
```

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### Example

The program while true do skip end refines (using unrestricted stuttering)

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but "the value of the counter is eventually 3" is obviously not preserved.

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but "the value of the counter is eventually 3" is obviously not preserved.

We can only permit finite stuttering.

Rather than adding self-loops, we allow **finite stuttering** through what essentially corresponds to lockstep simulation with finite unrollings of self-loops.



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This is achieved by making sure roles do not get "starved".

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If a thread takes a step in  $\mathcal{M}$  for role  $\rho$ , then  $\rho$  is refueled.

The Fairis logic manages the complexity using a resource

tid 
$$\mapsto \{\rho_1 \mapsto f_1, \dots, \rho_n \mapsto f_n\}$$

together with the Model( $\delta$ ) resource for the user-chosen model  $\mathcal{M}$ .



**Trillium** A higher-order separation logic framework for showing trace refinement between programs and models.

Aneris An instantiation of Trillium for reasoning about distributed systems.

- Single-decree Paxos refines its TLA+ model.

**Fairis** An instantiation of Trillium for proving termination of fine-grained concurrent programs under fair scheduling assumptions.

### Thank you!



### **Future Work**

- Fairis applies to (non-distributed) concurrent programs—fairness of distributed systems traces is a bit more subtle.
- Explore more constructions at the model level to allow for more modularity.
- More high-level reasoning principles for liveness reasoning.

#### Remark

- Logics (like Iris) based on step indexing fundamentally cannot prove liveness properties—at least directly.
- The Fairis approach sidesteps this issue entirely.
- No (entirely) free lunch: we have a "relative image-finiteness requirement" for the simulation relation. In practice, it has not (yet?) been an obstacle, but the restriction can be lifted with transfinite step indexing.