

ASYNCHRONOUS PROBABILISTIC COUPLINGS

in Higher-Order Separation Logic

Simon Oddershede Gregersen¹ Alejandro Aguirre¹ Philipp G. Haselwarter¹

Joseph Tassarotti² Lars Birkedal¹

¹Aarhus University, ²New York University

Motivating example

let $b = \text{flip in}$

$\lambda_. b$

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let r = ref(None) in  
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While this example seems esoteric, the pattern shows up in many places:
cryptographic security, hash functions, lazily-sampled big integers, ...

The pRHL approach

For such properties, people have developed **probabilistic relational Hoare logics**, where sampling statements are related through so-called coupling rules.

pRHL-couple

$$\frac{}{\{\text{True}\} \text{ flip } \sim \text{ flip } \{v_1, v_2. \exists(b : \mathbb{B}). b = v_1 = v_2\}}$$

However, it requires you to **“synchronize”** the probabilistic choices.

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This work

Goal: prove **contextual equivalence** of

- ... probabilistic programs written in an expressive programming language
- ... using a higher-order separation logic, called Clutch,
- ... and asynchronous probabilistic couplings

while mechanizing everything in the Coq proof assistant.

The $F_{\mu, \text{ref}}^{\text{rand}}$ language

An ML-like language with higher-order (recursive) functions, higher-order state, impredicative polymorphism, ..., and probabilistic uniform sampling.

$$e \in \text{Expr} ::= \dots \mid \text{rand}(e)$$
$$\tau \in \text{Type} ::= \alpha \mid \text{unit} \mid \text{bool} \mid \text{int} \mid \tau \times \tau \mid \tau + \tau \mid \tau \rightarrow \tau \mid \\ \forall \alpha. \tau \mid \exists \alpha. \tau \mid \mu \alpha. \tau \mid \text{ref } \tau$$

and a standard typing judgment $\Gamma \vdash e : \tau$.

Operational semantics

$$(\lambda x. e_1)e_2 \rightarrow^1 e_1[e_2/x]$$

$$\vdots$$

$$\text{rand}(N) \rightarrow^{1/(N+1)} n$$

$$n \in \{0, 1, \dots, N\}$$

For this presentation, we only consider $\text{flip} \triangleq \text{rand}(1)$.

Contextual refinement

The property of interest is **contextual refinement**.

$$\Gamma \vdash e_1 \lesssim_{\text{ctx}} e_2 : \tau \quad \triangleq \quad \forall \tau', (\mathcal{C} : (\Gamma \vdash \tau) \Rightarrow (\emptyset \vdash \tau')), \sigma. \\ \text{term}(\mathcal{C}[e_1], \sigma) \leq \text{term}(\mathcal{C}[e_2], \sigma)$$

and $\Gamma \vdash e_1 \simeq_{\text{ctx}} e_2 : \tau$ follows as refinement in both directions.

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2. A **logical refinement judgment** (a “logical” logical relation)

$$\Gamma \vDash e_1 \lesssim e_2 : \tau$$

that implies contextual refinement.

Refinement judgment

The judgment

$$\Gamma \vDash e_1 \lesssim e_2 : \tau$$

should be read as “in env. Γ , expression e_1 refines expression e_2 at type τ ”.

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Theorem (Fundamental)

If $\Gamma \vdash e : \tau$ then $\Gamma \vDash e \lesssim e : \tau$.

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Theorem (Fundamental)

If $\Gamma \vdash e : \tau$ then $\Gamma \vDash e \lesssim e : \tau$.

Theorem (Soundness)

If $\Gamma \vDash e_1 \lesssim e_2 : \tau$ then $\Gamma \vdash e_1 \lesssim_{\text{ctx}} e_2 : \tau$.

Symbolic execution rules

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$$\frac{e_1 \overset{\text{pure}}{\rightsquigarrow} e'_1 \quad \Gamma \vDash K[e'_1] \lesssim e_2 : \tau}{\Gamma \vDash K[e_1] \lesssim e_2 : \tau}$$

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Relational separation logic

Clutch is built on top of the **Iris separation logic framework**

$P, Q \in \text{iProp} ::= \text{True} \mid \text{False} \mid P \wedge Q \mid P \vee Q \mid P \Rightarrow Q \mid$	(propositional)
$\forall x. P \mid \exists x. P \mid$	(higher-order)
$P * Q \mid P \multimap Q \mid \ell \mapsto v \mid$	(separation)
$\Box P \mid \triangleright P \mid \boxed{a} \mid \boxed{P} \mid \dots \mid$	(Iris)
$\text{wp } e \{ \Phi \} \mid \text{spec}(e) \mid \dots$	(Clutch)

within which we derive $\Gamma \vDash e_1 \lesssim e_2 : \tau$.

- ▶ The connectives $\text{wp } e \{ \Phi \}$ and $\text{spec}(e)$ form a **coupling logic**.

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- ▶ A pRHL-style quadruple $\{P\} e_1 \sim e_2 \{Q\}$ can be encoded as

$$P \text{ -* spec}(e_2) \text{ -* wp } e_1 \{v_1. \exists v_2. \text{spec}(v_2) * Q(v_1, v_2)\}$$

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- ▶ **Soundness** of the coupling logic will allow us to conclude that there exists a **probabilistic coupling** of the execution of e_1 and e_2 .

Asynchronous couplings

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$$\begin{aligned}\rho \in \text{Cfg} &::= \text{Expr} \times \text{State} \\ \sigma \in \text{State} &::= \text{Heap} \times \text{Tapes}\end{aligned}$$

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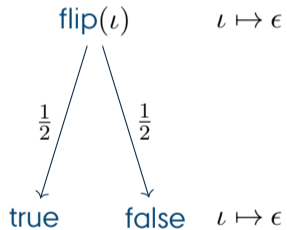
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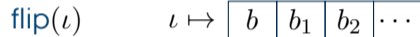
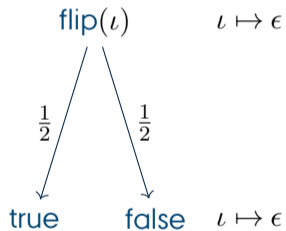
Presampling tapes

$\text{flip}(l)$ $l \mapsto \epsilon$

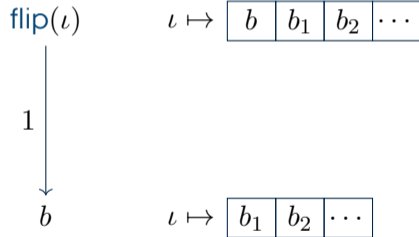
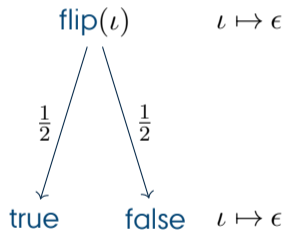
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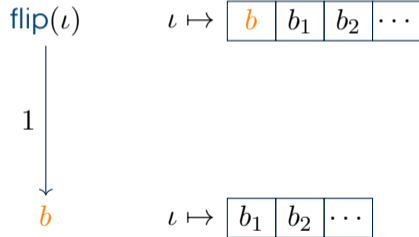
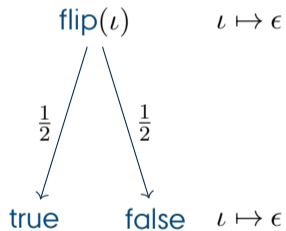
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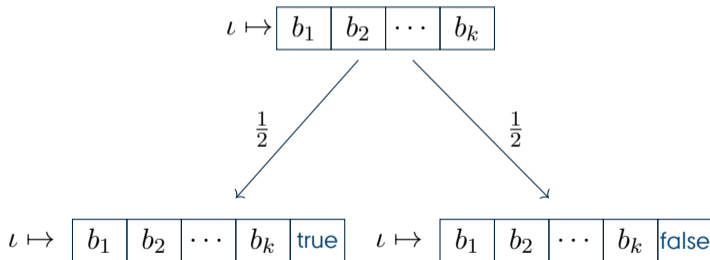
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It—locally—turns reasoning about probabilistic choice into **reasoning about state**.

Asynchronous couplings

With presampling tapes, we can synchronously couple tape samplings with program samplings

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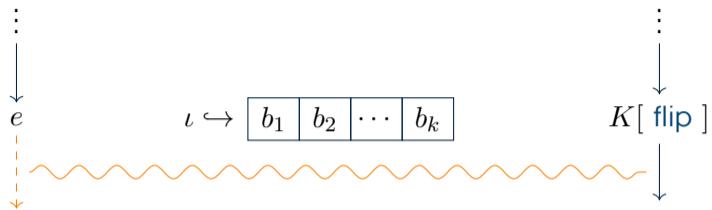
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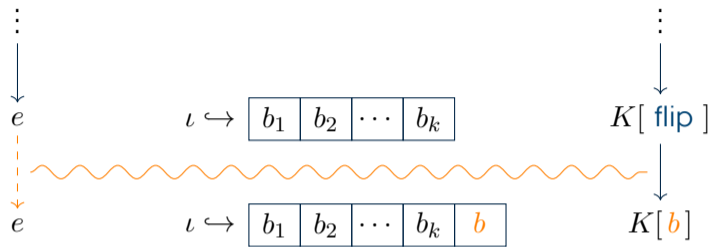
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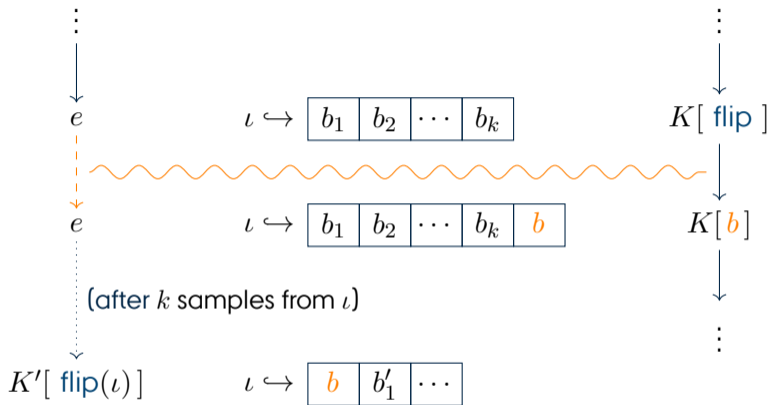
\vdots
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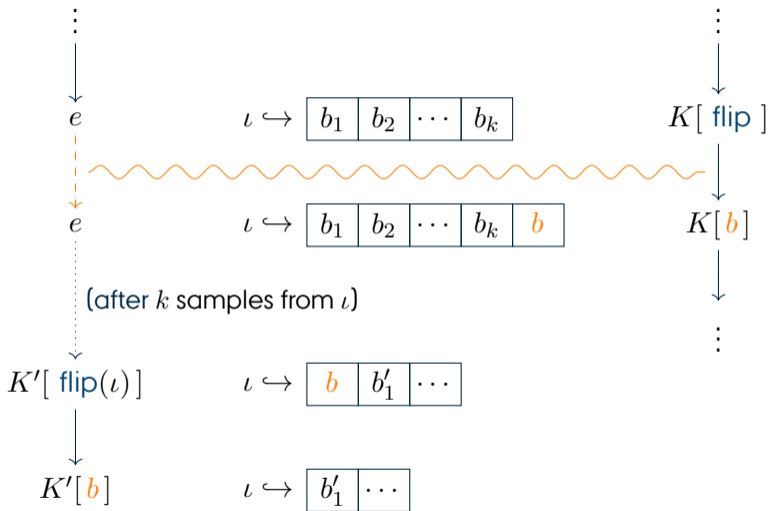
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-------	-------	----------	-------

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 $K[\text{flip}]$









Motivating example cont'd

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```

```
 $\lambda\_.$  match ! $r$  with
```

```
  Some( $b$ )  $\Rightarrow b$ 
```

```
  | None     $\Rightarrow$  let  $b = \text{flip}$  in  
                   $r \leftarrow \text{Some}(b)$ ;  
                   $b$ 
```

```
end
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\approx_{ctx}

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 $\lambda\_.$   $b$ 
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let  $\iota$  = tape in
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  | None   ⇒ let b = flip( $\iota$ ) in  $\lesssim_{\text{ctx}}$   
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end
```

\approx_{ctx} let b = flip in $\lambda_. b$

Summary

- ▶ A higher-order **probabilistic relational separation logic**, Clutch, for proving contextual refinement of higher-order probabilistic programs.
- ▶ A proof method for **asynchronous couplings**.
- ▶ Many more examples in the paper:
 - Cryptographic security, hash functions, lazily-sampled big integers, ...
- ▶ Full mechanization of all results in the Coq proof assistant.

Contact	gregersen@cs.au.dk
Paper	https://doi.org/10.1145/3632868
Coq dev.	https://github.com/logsem/clutch

Extras

Let $\text{step}(\rho) \in \mathcal{D}(\text{Cfg})$ be the distribution of single step reduction of $\rho \in \text{Cfg}$.

$$\text{exec}_n(e, \sigma) \triangleq \begin{cases} \mathbf{0} & \text{if } e \notin \text{Val and } n = 0 \\ \text{ret}(e) & \text{if } e \in \text{Val} \\ \text{step}(e, \sigma) \gg \text{exec}_{(n-1)} & \text{otherwise} \end{cases}$$

$$\text{exec}(\rho)(v) \triangleq \lim_{n \rightarrow \infty} \text{exec}_n(\rho)(v)$$

$$\text{term}(\rho) \triangleq \sum_{v \in \text{Val}} \text{exec}(\rho)(v)$$

Definition (Coupling)

Let $\mu_1 \in \mathcal{D}(A)$, $\mu_2 \in \mathcal{D}(B)$. A sub-distribution $\mu \in \mathcal{D}(A \times B)$ is a coupling of μ_1 and μ_2 if

1. $\forall a. \sum_{b \in B} \mu(a, b) = \mu_1(a)$
2. $\forall b. \sum_{a \in A} \mu(a, b) = \mu_2(b)$

Given a relation $R \subseteq A \times B$ we say μ is an R -coupling if furthermore $\text{supp}(\mu) \subseteq R$. We write $\mu_1 \sim \mu_2 : R$ if there exists an R -coupling of μ_1 and μ_2 .

Lemma

If $\mu_1 \sim \mu_2 : (=)$ then $\mu_1 = \mu_2$.

Definition (Left-Partial Coupling)

Let $\mu_1 \in \mathcal{D}(A)$, $\mu_2 \in \mathcal{D}(B)$. A sub-distribution $\mu \in \mathcal{D}(A \times B)$ is a left-partial coupling of μ_1 and μ_2 if

1. $\forall a. \sum_{b \in B} \mu(a, b) = \mu_1(a)$
2. $\forall b. \sum_{a \in A} \mu(a, b) \leq \mu_2(b)$

Given a relation $R \subseteq A \times B$ we say μ is an R -left-partial-coupling if furthermore $\text{supp}(\mu) \subseteq R$. We write $\mu_1 \lesssim \mu_2 : R$ if there exists an R -left-partial-coupling of μ_1 and μ_2 .

Lemma

If $\mu_1 \sim \mu_2 : R$ then $\mu_1 \lesssim \mu_2 : R$.

Lemma

If $\mu_1 \lesssim \mu_2 : (=)$ then $\forall a. \mu_1(a) \leq \mu_2(a)$.

The adequacy theorem relies on the fact that presampling does not matter.

Lemma (Erasure)

If $\sigma_1(\iota) \in \text{dom}(\sigma_1)$ then

$$\text{exec}_n(e_1, \sigma_1) \sim (\text{step}_\iota(\sigma_1) \gg \lambda\sigma_2. \text{exec}_n(e_1, \sigma_2)) : (=)$$